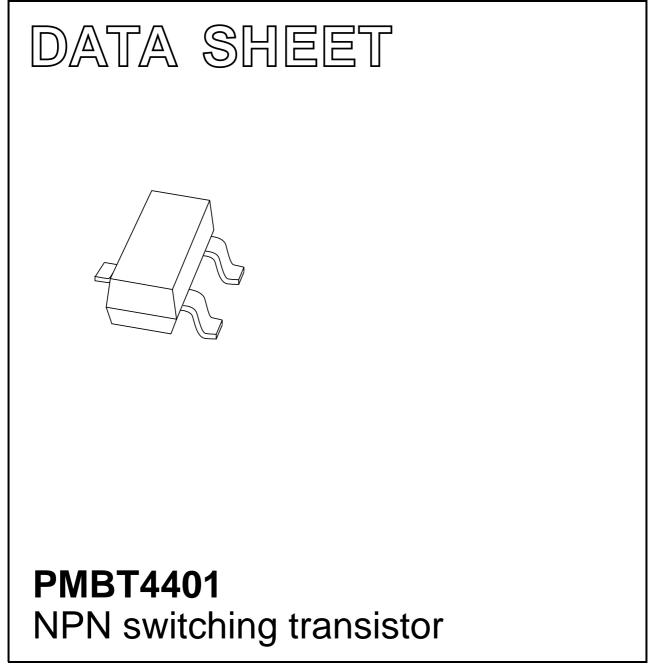
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 21



Product data sheet

NPN switching transistor

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

APPLICATIONS

• Industrial and consumer switching applications.

DESCRIPTION

NPN switching transistor in a SOT23 plastic package. PNP complement: PMBT4403.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
PMBT4401	*2X	

Note

- 1. * = p: Made in Hong Kong.
 - * = t : Made in Malaysia.

* = W : Made in China.

ORDERING INFORMATION

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	

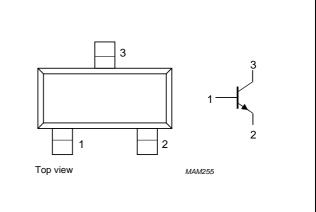


Fig.1 Simplified outline (SOT23) and symbol.

TYPE	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
PMBT4401	_	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	60	V
V _{CEO}	collector-emitter voltage	open base	-	40	V
V _{EBO}	emitter-base voltage	open collector	—	6	V
I _C	collector current (DC)		—	600	mA
I _{CM}	peak collector current		-	800	mA
I _{BM}	peak base current		—	200	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \text{ °C}; \text{ note } 1$	—	250	mW
T _{stg}	storage temperature		-65	+150	°C
Тj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th (j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

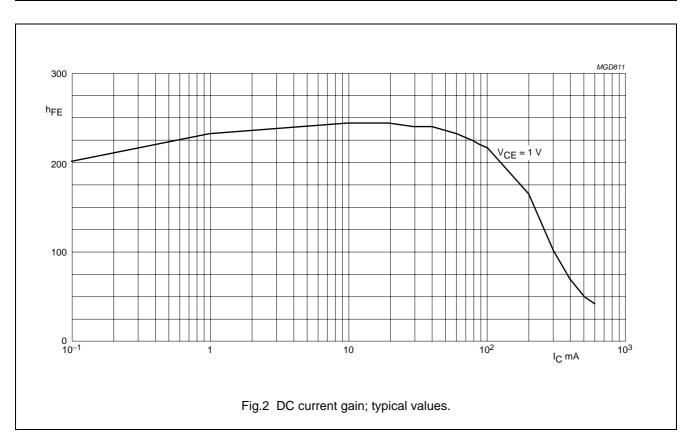
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = 60 V	-	50	nA
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = 6 V	-	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; (see Fig.2)			
		I _C = 0.1 mA	20	-	
		$I_{\rm C} = 1 \rm{mA}$	40	-	
		I _C = 10 mA	80	_	
		I _C = 150 mA; note 1	100	300	
		I _C = 500 mA; V _{CE} = 2 V; note 1	40	-	
V _{CEsat}	collector-emitter saturation	$I_{C} = 150 \text{ mA}; I_{B} = 15 \text{ mA}; \text{ note } 1$	_	400	mV
voltage	voltage	$I_{\rm C} = 500 \text{ mA}; I_{\rm B} = 50 \text{ mA}; \text{ note } 1$	_	750	mV
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C}$ = 150 mA; $I_{\rm B}$ = 15 mA; note 1	-	950	mV
		$I_{C} = 500 \text{ mA}; I_{B} = 50 \text{ mA}; \text{ note } 1$	_	1.2	V
C _c	collector capacitance	$I_{E} = I_{e} = 0; V_{CB} = 5 V; f = 1 MHz$	-	8	pF
C _e	emitter capacitance	$I_{C} = I_{c} = 0; V_{EB} = 500 \text{ mV}; f = 1 \text{ MHz}$	-	30	pF
f _T	transition frequency	I _C = 20 mA; V _{CE} = 10 V; f = 100 MHz	250	_	MHz
Switching t	imes (between 10% and 90% leve	• !s); (see Fig.3)			
t _{on}	turn-on time	I _{Con} = 150 mA; I _{Bon} = 15 mA;	-	35	ns
t _d	delay time	I _{Boff} = –15 mA	-	15	ns
t _r	rise time	1	-	20	ns
t _{off}	turn-off time	7	-	250	ns
t _s	storage time	7	-	200	ns
t _f	fall time		-	60	ns

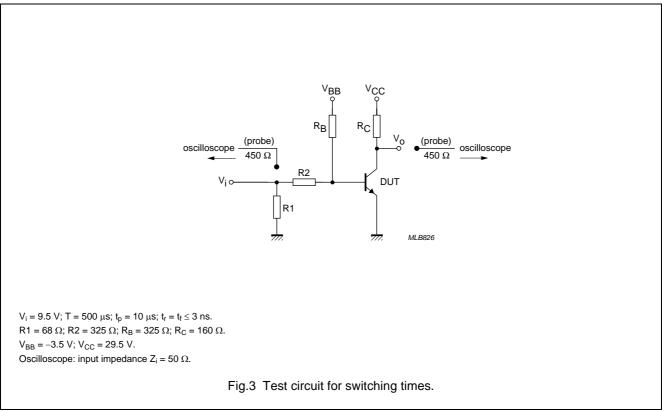
Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

PMBT4401

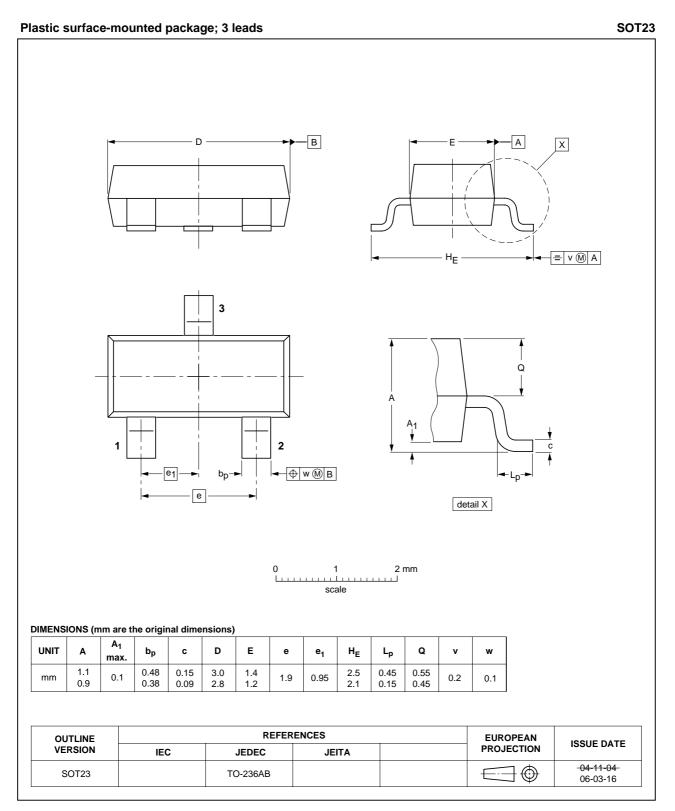
NPN switching transistor





NPN switching transistor

PACKAGE OUTLINE



PMBT4401

NPN switching transistor

PMBT4401

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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Printed in The Netherlands

R75/04/pp7

Date of release: 2004 Jan 21

Document order number: 9397 750 12462

