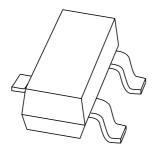
DISCRETE SEMICONDUCTORS

DATA SHEET



PMBTA06 NPN general purpose transistor

Product data sheet Supersedes data of 1999 Apr 29 2004 Jan 22



NXP Semiconductors Product data sheet

NPN general purpose transistor

PMBTA06

FEATURES

• High current (max. 500 mA)

• Low voltage (max. 80 V).

APPLICATIONS

• General purpose switching and amplification in e.g. telephony and professional communication equipment.

DESCRIPTION

NPN transistor in a SOT23 plastic package. PNP complement: PMBTA56.

MARKING

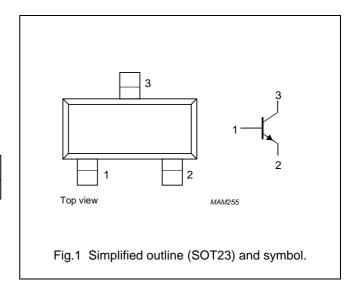
TYPE NUMBER	MARKING CODE(1)	
PMBTA06	*1G	

Note

* = p : Made in Hong Kong.
 * = t : Made in Malaysia.
 * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
PMBTA06	_	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	80	V
V _{CEO}	collector-emitter voltage	open base	_	80	V
V _{EBO}	emitter-base voltage	open collector	_	4	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	1	Α
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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NPN general purpose transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 80 V	_	50	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	50	nA
h _{FE}	DC current gain	I _C = 10 mA; V _{CE} = 1 V	100	_	
		I _C = 100 mA; V _{CE} = 1 V	100	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 10 mA	_	0.25	V
V_{BE}	base-emitter voltage	I _C = 100 mA; V _{CE} = 1 V	_	1.2	V
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 2 \text{ V}; f = 100 \text{ MHz}$	100	_	MHz

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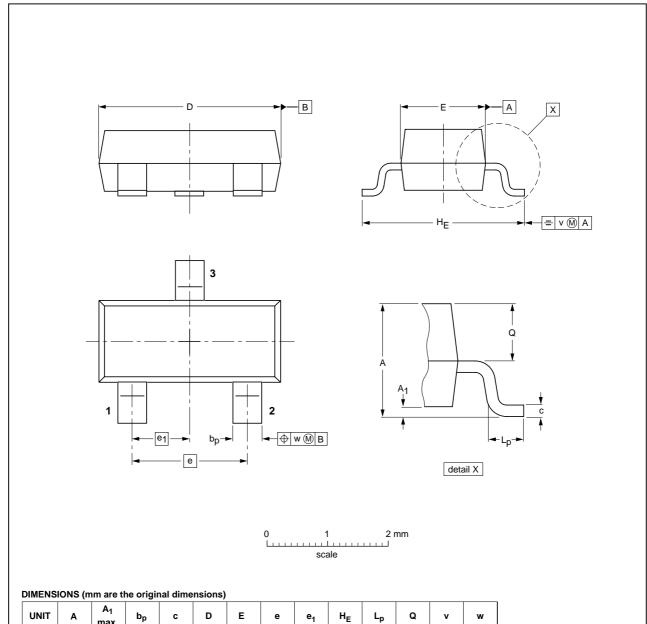
NPN general purpose transistor

PMBTA06

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04- 06-03-16

0.45

0.55

0.1

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max

0.38

0.9

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NPN general purpose transistor

PMBTA06

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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