PMEG4020ETR



40 V, 2 A low VF MEGA Schottky barrier rectifier
Rev. 1 — 5 October 2011 Pro

Product data sheet

Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

1.2 Features and benefits

- Average forward current: I_{F(AV)} ≤ 2 A
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage
- High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T_i ≤ 175 °C

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- High temperature applications

1.4 Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--------------------|-------------------------|---|------------|-----|-----|-----|------|
| I _{F(AV)} | average forward current | square wave; δ = 0.5; f = 20 kHz; $T_{amb} \le 110$ °C | <u>[1]</u> | - | - | 2 | Α |
| | | square wave; δ = 0.5; f = 20 kHz; $T_{sp} \le 165$ °C | | - | - | 2 | Α |
| V_R | reverse voltage | T _j = 25 °C | | - | - | 40 | V |
| V_{F} | forward voltage | I _F = 2 A; T _j = 25 °C | | - | 430 | 490 | mV |
| I _R | reverse current | $V_R = 40 \text{ V}; T_j = 25 \text{ °C}$ | | - | 25 | 100 | μΑ |

^[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.



2. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|----------------|
| 1 | K | cathode[1] | | . 54 . |
| 2 | Α | anode | 1 2 | 1 🔼 2 |
| | | | SOD123W | sym001 |

^[1] The marking bar indicates the cathode.

3. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|-------------|---------|--|---------|
| | Name | Description | Version |
| PMEG4020ETR | - | plastic surface-mounted package; 2 leads | SOD123W |

4. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PMEG4020ETR | C1 |

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|--------------------|-------------------------------------|---|------------|-----|------|------|
| V_R | reverse voltage | T _j = 25 °C | | - | 40 | V |
| I _{F(AV)} | average forward current | square wave; δ = 0.5; f = 20 kHz; T _{amb} ≤ 110 °C | <u>[1]</u> | - | 2 | Α |
| | | square wave; δ = 0.5; f = 20 kHz; T _{sp} ≤ 165 °C | | - | 2 | Α |
| I _{FSM} | non-repetitive peak forward current | square wave; $t_p = 8 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$ | | - | 50 | А |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2][3] | - | 680 | mW |
| | | | [4][3] | - | 1150 | mW |
| | | | [1][3] | - | 2140 | mW |
| Tj | junction temperature | | | - | 175 | °C |
| T _{amb} | ambient temperature | | | -55 | 175 | °C |
| T _{stg} | storage temperature | | | -65 | 175 | °C |
| | | | | | | |

^[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|-----------------------|--|-------------|------------|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance | in free air | [1][2][3] | - | - | 220 | K/W |
| | from junction to ambient | | [1][4][3] | - | - | 130 | K/W |
| | ambient | | [1][5][3] | - | - | 70 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | <u>[6]</u> | - | - | 18 | K/W |

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Reflow soldering is the only recommended soldering method.

^[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

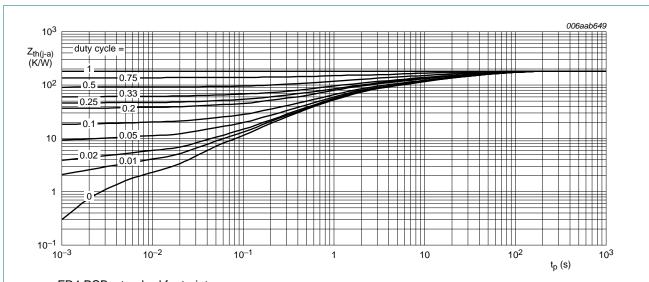
^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Reflow soldering is the only recommended soldering method.

^[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

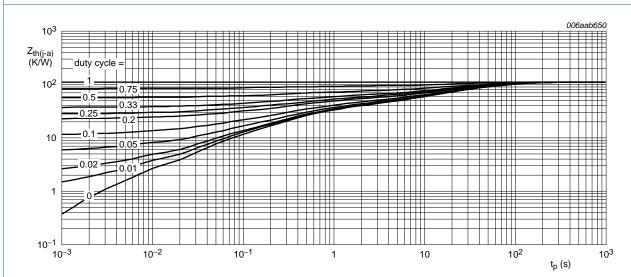
^[5] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

^[6] Soldering point of cathode tab.



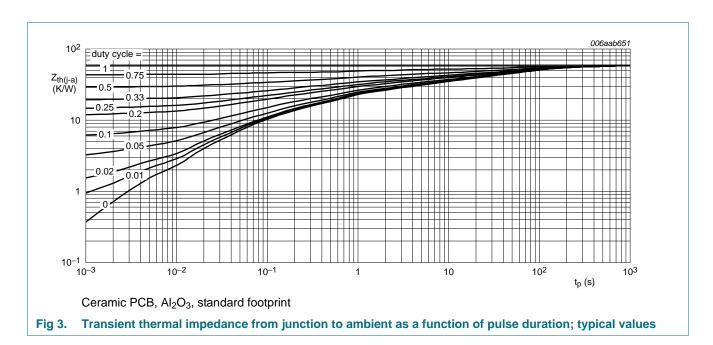
FR4 PCB, standard footprint

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

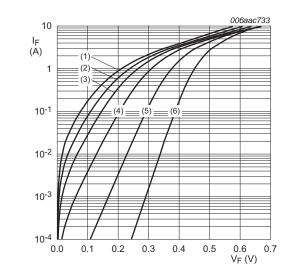
Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



7. Characteristics

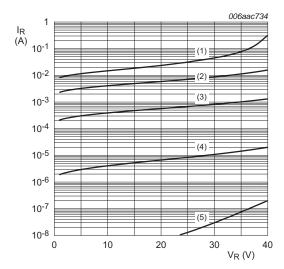
Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------------|-------------------|--|-----|-----|-----|------|
| V_{F} | forward voltage | $I_F = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$ | - | 295 | 330 | mV |
| | | I _F = 1 A; T _j = 25 °C | - | 380 | 440 | mV |
| | | I _F = 2 A; T _j = 25 °C | - | 430 | 490 | mV |
| | | I _F = 2 A; T _j = 125 °C | - | 330 | 380 | mV |
| I _R reverse cui | reverse current | $V_R = 10 \text{ V}; T_j = 25 ^{\circ}\text{C}$ | - | 5 | - | μΑ |
| | | $V_R = 40 \text{ V}; T_j = 25 ^{\circ}\text{C}$ | - | 25 | 100 | μΑ |
| | | $V_R = 10 \text{ V}; T_j = 125 ^{\circ}\text{C}$ | - | 4 | - | mA |
| | | $V_R = 40 \text{ V}; T_j = 125 ^{\circ}\text{C}$ | - | 15 | - | mA |
| C _d | diode capacitance | $V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$ | - | 250 | - | pF |
| | | $V_R = 10 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$ | - | 95 | - | pF |
| | | | | | | |



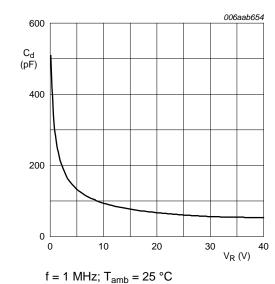
- (1) $T_i = 175 \, ^{\circ}C$
- (2) $T_j = 150 \, ^{\circ}\text{C}$
- (3) $T_i = 125 \, ^{\circ}C$
- (4) $T_i = 85 \, ^{\circ}C$
- (5) $T_j = 25 \, ^{\circ}C$
- (6) $T_i = -40 \, ^{\circ}\text{C}$

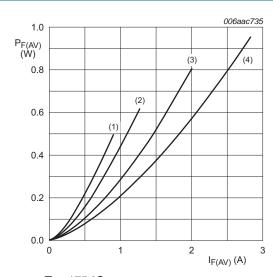
Fig 4. Forward current as a function of forward voltage; typical values



- (1) $T_j = 150 \, ^{\circ}C$
- (2) $T_j = 125 \, ^{\circ}C$
- (3) $T_j = 85 \, ^{\circ}C$
- (4) $T_j = 25 \, ^{\circ}C$
- (5) $T_j = -40 \, ^{\circ}\text{C}$

Fig 5. Reverse current as a function of reverse voltage; typical values



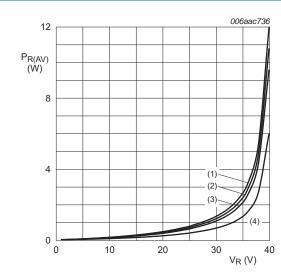


T_i = 175 °C

- (1) $\delta = 0.1$
- (2) $\delta = 0.2$
- (3) $\delta = 0.5$
- (4) $\delta = 1.0$

Diode capacitance as a function of reverse Fig 6. voltage; typical values

Fig 7. Average forward power dissipation as a function of average forward current; typical values



T_i = 150 °C

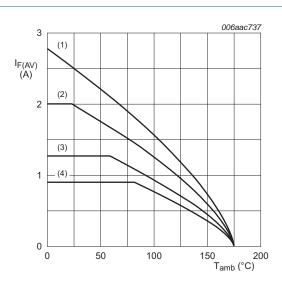
 $(1) \delta = 1.0$

(2) $\delta = 0.9$

(3) $\delta = 0.8$

(4) $\delta = 0.5$

Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T_i = 175 °C

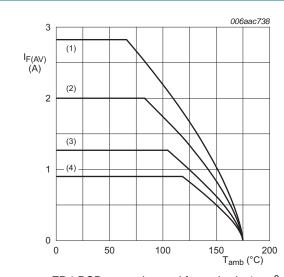
(1) $\delta = 1.0 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 \mbox{cm}^2

T_i = 175 °C

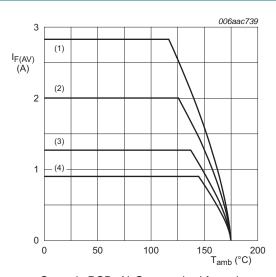
 $(1) \delta = 1.0$

(2) $\delta = 0.9$

(3) $\delta = 0.8$

 $(4) \delta = 0.5$

Fig 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$

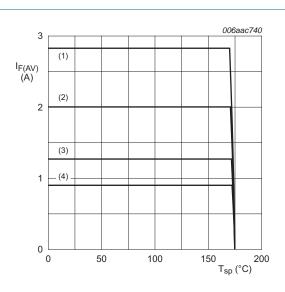
(1) $\delta = 1.0 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig 11. Average forward current as a function of ambient temperature; typical values



T_i = 175 °C

(1) $\delta = 1.0$

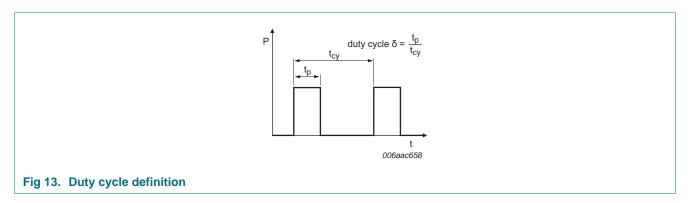
(2) $\delta = 0.9$

(3) $\delta = 0.8$

(4) $\delta = 0.5$

Fig 12. Average forward current as a function of solder point temperature; typical values

8. Test information

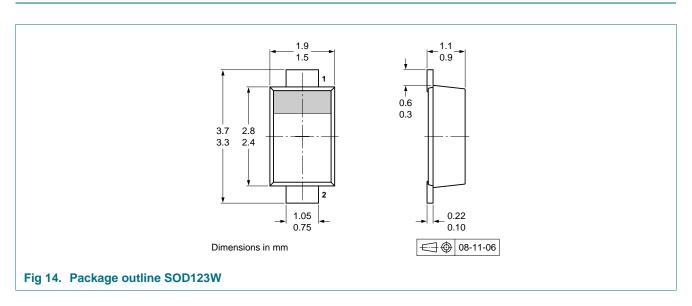


The current ratings for the typical waveforms as shown in figures $\underline{9}$, $\underline{10}$, $\underline{11}$ and $\underline{12}$ are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

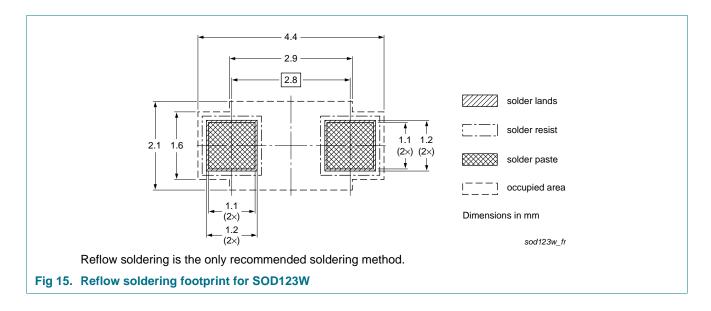
Table 8. Ordering information

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

| Type number | Package | Description | Packing quantity 3000 |
|-------------|---------|--------------------------------|-----------------------|
| PMEG4020ETR | SOD123W | 4 mm pitch, 8 mm tape and reel | -115 |

^[1] For further information and the availability of packing methods, see 14 "Contact information".

11. Soldering





12. Revision history

Table 9. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-----------------|--------------|--------------------|---------------|------------|
| PMEG4020ETR v.1 | 20111005 | Product data sheet | - | - |

13. Legal information

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| Document status [1] [2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
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- [2] The term 'short data sheet' is explained in section "Definitions"
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