

MICROPOWER SUPPLY VOLTAGE SUPERVISORS

 Check for Samples: [TLC7701-EP](#), [TLC7705-EP](#), [TLC7733-EP](#)

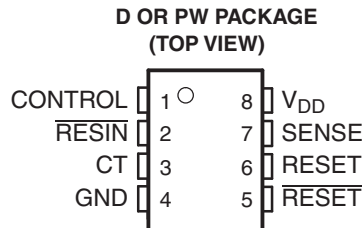
FEATURES

- Power-On Reset Generator
- Automatic Reset Generation After Voltage Drop
- Precision Voltage Sensor
- Temperature-Compensated Voltage Reference
- Programmable Delay Time by External Capacitor
- Supply Voltage Range . . . 2 V to 6 V
- Defined RESET Output from $V_{DD} \geq 1$ V
- Power-Down Control Support for Static RAM With Battery Backup
- Maximum Supply Current of 16 mA
- Power Saving Totem-Pole Outputs

SUPPORTS DEFENSE, AEROSPACE, AND MEDICAL APPLICATIONS

- Controlled Baseline
- One Assembly/Test Site
- One Fabrication Site
- Available in Extended ($-40^{\circ}\text{C}/125^{\circ}\text{C}$ and $-55^{\circ}\text{C}/125^{\circ}\text{C}$), Temperature Ranges⁽¹⁾
- Extended Product Life Cycle
- Extended Product-Change Notification
- Product Traceability

(1) Additional temperature ranges available - contact factory



DESCRIPTION

The TLC77xx family of micropower supply voltage supervisors provide reset control, primarily in microcomputer and microprocessor systems.

During power-on, RESET is asserted when V_{DD} reaches 1 V. After minimum V_{DD} (≥ 2 V) is established, the circuit monitors SENSE voltage and keeps the reset outputs active as long as SENSE voltage ($V_{I(\text{SENSE})}$) remains below the threshold voltage. An internal timer delays return of the output to the inactive state to ensure proper system reset. The delay time (t_d) is determined by an external capacitor:

$$t_d = 2.1 \times 10^4 \times C_T \quad (1)$$

Where

C_T is in farads

t_d is in seconds

Except for the TLC7701, which can be customized with two external resistors, each supervisor has a fixed sense threshold voltage set by an internal voltage divider. When SENSE voltage drops below the threshold voltage, the outputs become active and stay in that state until SENSE voltage returns above threshold voltage and the delay time (t_d) has expired.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

In addition to the power-on reset and undervoltage-supervisor function, the TLC77xx adds power-down control support for static RAM. When CONTROL is tied to GND, RESET will act as active high. The voltage monitor contains additional logic intended for control of static memories with battery backup during power failure. By driving the chip select (\overline{CS}) of the memory circuit with the RESET output of the TLC77xx and with the CONTROL driven by the memory bank select signal ($\overline{CSH1}$) of the microprocessor (see Figure 11), the memory circuit is automatically disabled during a power loss. (In this application the TLC77xx power has to be supplied by the battery.)

ORDERING INFORMATION

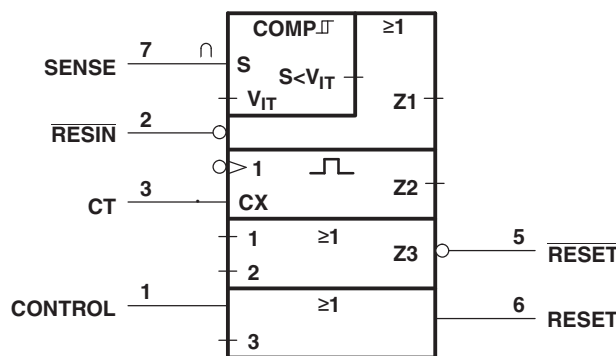
T_A	PACKAGE ⁽¹⁾		ORDERABLE PART NUMBER	TOP-SIDE MARKING	VID NUMBER
-40°C to 125°C	TSSOP - PW	Tape and reel	TLC7701QPWREP	7701QE	V62/04604 - 01XE
			TLC7705QPWREP	7705QE	V62/04604 - 02XE
			TLC7733QPWREP	7733QE	V62/04604 - 03XE
-55°C to 125°C	TSSOP - PW	Tape and reel	TLC7701MPWREP	7701ME	V62/04604 - 04XE
			TLC7733MPWREP	7733ME	V62/04604 - 06XE
	SOIC - D	Tape and reel	TLC7701MDREP	7701ME	V62/04604 - 04YE

(1) The PW package is only available left-end taped and reeled (indicated by the R suffix on the device type; e.g., TLC7701QPWREP).

Table 1. FUNCTION TABLE

CONTROL	RESIN	$V_{I(SENSE)} > V_{IT+}$	RESET	\overline{RESET}
L	L	False	H	L
L	L	True	H	L
L	H	False	H	L
L	H	True	L ⁽¹⁾	H ⁽¹⁾
H	L	False	H	L
H	L	True	H	L
H	H	False	H	L
H	H	True	H	H ⁽¹⁾

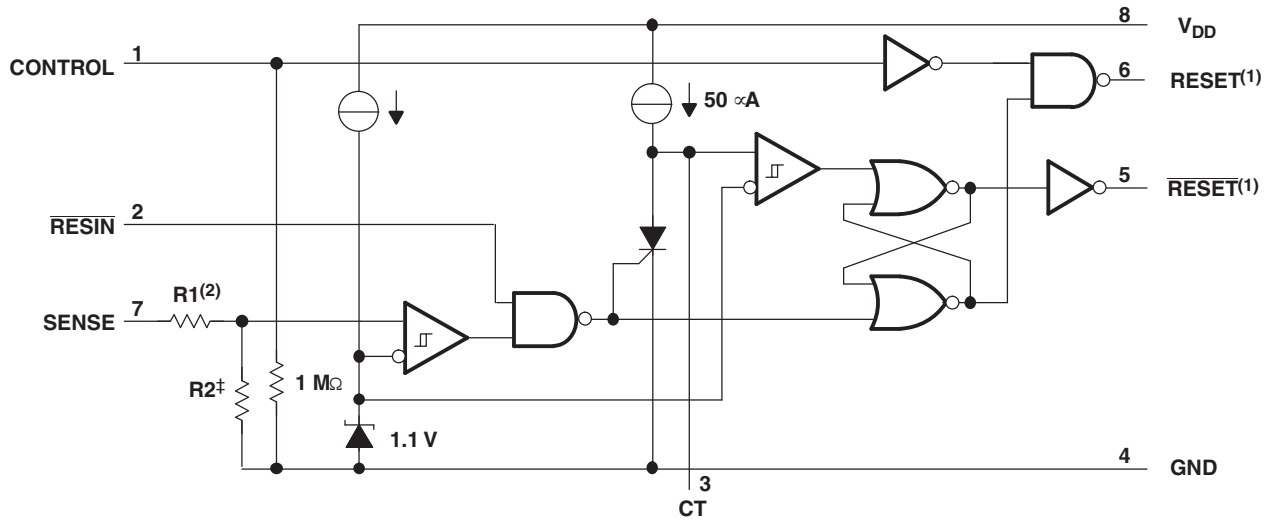
(1) RESET and \overline{RESET} states shown are valid for $t > t_d$.



(1) This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Figure 1. Logic Symbol

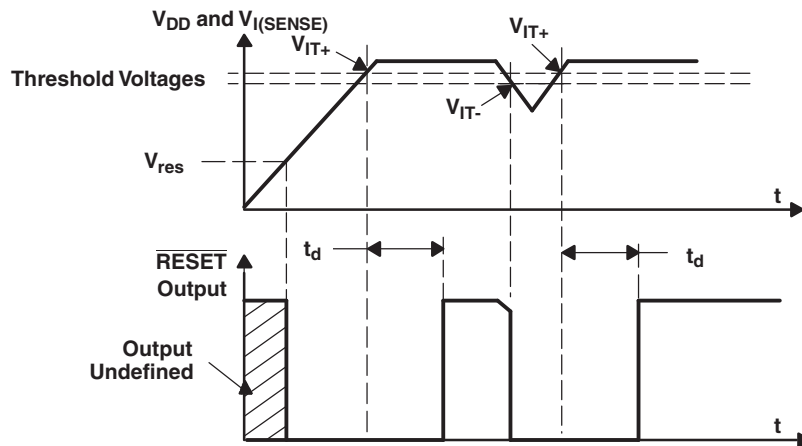
FUNCTIONAL BLOCK DIAGRAM



- (1) Outputs are totem-pole configuration. External pullup or pulldown resistors are not required.
- (2) Nominal values:

	R1 (Typ)	R2 (Typ)
TLC7701	0	∞
TLC7705	910 k Ω	290 k Ω
TLC7733	750 k Ω	450 k Ω

TIMING DIAGRAM



ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

		VALUE	UNIT
V _{DD}	Supply voltage ⁽²⁾	7	V
Input voltage range, CONTROL, RESIN, SENSE ⁽²⁾		-0.3 to 7	V
I _{OL}	Maximum low output current	10	mA
I _{OH}	Maximum high output current	10	mA
I _{IK}	Input clamp current, (V _I < 0 or V _I > V _{DD})	±10	mA
I _{OK}	Output clamp current, (V _O < 0 or V _O > V _{DD})	±10	mA
T _A	Operating free-air temperature range	TL77xxQ	-40 to 125
		TL77xxM	-55 to 125
T _{stg}	Storage temperature range	-65 to 150	°C

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltage values are with respect to GND.

THERMAL INFORMATION

THERMAL METRIC ⁽¹⁾		TLC77xx-EP	TLC77xx-EP	UNITS
		D	PW	
		8 PINS	8 PINS	
θ _{JA}	Junction-to-ambient thermal resistance	97.1	168	°C/W
θ _{JC}	Junction-to-case thermal resistance	39.4	38.9	
θ _{JB}	Junction-to-board thermal resistance	-	96.6	
ψ _{JT}	Junction-to-top characterization parameter	-	1.5	
ψ _{JB}	Junction-to-board characterization parameter	-	94.7	

- (1) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, [SPRA953](#).

RECOMMENDED OPERATING CONDITIONS⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{DD}	Supply voltage	2		6	V
V _I	Input voltage	0		V _{DD}	V
V _{IH}	High-level input voltage at $\overline{\text{RESIN}}$ and CONTROL ⁽²⁾	0.7×V _{DD}			V
V _{IL}	Low-level input voltage at $\overline{\text{RESIN}}$ and CONTROL			0.2×V _{DD}	V
I _{OH}	High-level output current, V _{DD} ≥ 2.7 V			-2	mA
I _{OL}	Low-level output current, V _{DD} ≥ 2.7 V			2	mA
Δt/ΔV	Input transition rise and fall rate at $\overline{\text{RESIN}}$ and CONTROL			100	ns/V
T _A	Operating free-air temperature range	Q temperature range	-40	125	°C
		M temperature range	-55	125	

- (1) Long-term high-temperature storage and/or extended use at maximum recommended operating conditions may result in a reduction of overall device life. See http://www.ti.com/ep_quality for additional information on enhanced plastic packaging.
- (2) To ensure a low supply current, V_{IL} should be kept <0.3 V and V_{IH} > -0.3 V.

ELECTRICAL CHARACTERISTICS

 over recommended operating conditions⁽¹⁾ (unless otherwise noted)

PARAMETER			TEST CONDITIONS	T _A = -40°C to 125°C			T _A = -55°C to 125°C			UNIT	
				MIN	TYP ⁽²⁾	MAX	MIN	TYP ⁽²⁾	MAX		
V _{OH}	High-level output voltage	I _{OH} = - 20 μA	V _{DD} = 2 V	1.8			1.8			V	
			V _{DD} = 2.7 V	2.5			2.5				
			V _{DD} = 4.5 V	4.3			4.3				
		I _{OH} = - 20 mA	V _{DD} = 4.5 V	3.7			3.7				
V _{OL}	Low-level output voltage	I _{OH} = - 20 μA	V _{DD} = 2 V	0.2			0.2			V	
			V _{DD} = 2.7 V	0.2			0.2				
			V _{DD} = 4.5 V	0.2			0.2				
		I _{OH} = - 20 mA	V _{DD} = 4.5 V	0.5			0.5				
V _{IT-}	Negative-going input threshold voltage, SENSE ⁽³⁾	TLC7701	V _{DD} = 2 V to 6 V	1.04	1.1	1.16				V	
		TLC7705		4.43	4.5	4.63					
		TLC7733		2.855	2.93	3.03	2.8	2.93	3.03		
V _{hys}	Hysteresis voltage, SENSE	TLC7701	V _{DD} = 2 V to 6 V	30						mV	
		TLC7705		70							
		TLC7733		70			70				
V _{res}	Power-up reset voltage ⁽⁴⁾	I _{OL} = 20 μA				1			V		
I _I	Input current	RESIN	V _I = 0 V to V _{DD}	2			2			μA	
		CONTROL	V _I = V _{DD}	7			15				
		SENSE	V _I = 5 V	5			10				
		SENSE, TLC7701 only	V _I = 5 V	2							
I _{DD}	Supply current	RESIN = V _{DD} , SENSE = V _{DD} ≥ V _{ITmax} + 0.2 V, CONTROL = 0 V, Outputs open	9			16			9	18	μA
I _{DD(d)}	Supply current during t _d	V _{DD} = 5 V, V _{CT} = 0 , RESIN = V _{DD} , SENSE = V _{DD} , CONTROL = 0 V, Outputs open	120			150			120	150	μA
C _I	Input capacitance, SENSE	V _I = 0 V to V _{DD}	50			50			50		pF

 (1) All characteristics are measured with C_T = 0.1 μF.

 (2) Typical values apply at T_A = 25°C.

(3) To ensure best stability of the threshold voltage, a bypass capacitor (ceramic, 0.1 μF) should be connected near the supply terminals.

 (4) The lowest supply voltage at which RESET becomes active. The symbol V_{res} is not currently listed within EIA or JEDEC standards for semiconductor symbology. Rise time of V_{DD} ≥ 15 ms/V.

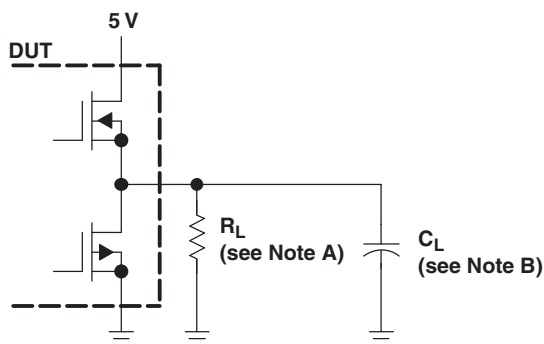
SWITCHING CHARACTERISTICS

over operating free-air temperature range (unless otherwise noted)

PARAMETER		MEASURED		TEST CONDITIONS	T _A = -40°C to 125°C			T _A = -55°C to 125°C			UNIT
		FROM (INPUT)	TO (OUTPUT)		MIN	TYP	MAX	MIN	TYP	MAX	
t _d	Delay time			RESIN = 0.7 × V _{DD} , CONTROL = 0.2 × V _{DD} , C _T = 100 nF, T _A = Full range, See timing diagram	1.1	2.1	4.2		2.1		ms
t _{PLH}	Propagation delay time, low-to-high level output	SENSE	$\overline{\text{RESET}}$	V _{IH} = V _{IT+max} + 0.2 V, V _{IL} = V _{IT-min} - 0.2 V, RESIN = 0.7 × V _{DD} , CONTROL = 0.2 × V _{DD} , C _T = NC ⁽¹⁾			20			20	μs
t _{PLH}	Propagation delay time, high-to-low level output						5		5		
t _{PLH}	Propagation delay time, low-to-high level output		RESET				5		5		
t _{PLH}	Propagation delay time, high-to-low level output						20		20		
t _{PLH}	Propagation delay time, low-to-high level output	$\overline{\text{RESIN}}$	$\overline{\text{RESET}}$	V _{IH} = 0.7 × V _{DD} , V _{IL} = 0.2 × V _{DD} , SENSE = V _{IT+max} + 0.2 V, CONTROL = 0.2 × V _{DD} , C _T = NC ⁽¹⁾			20			20	μs
t _{PLH}	Propagation delay time, high-to-low level output						60		60	ns	
t _{PLH}	Propagation delay time, low-to-high level output		RESET				65		65		
t _{PLH}	Propagation delay time, high-to-low level output						20		20	μs	
t _{PLH}	Propagation delay time, low-to-high level output	CONTROL	RESET	V _{IH} = 0.7 × V _{DD} , V _{IL} = 0.2 × V _{DD} , SENSE = V _{IT+max} + 0.2 V, RESIN = 0.7 × V _{DD} , C _T = NC ⁽¹⁾			58			58	ns
t _{PLH}	Propagation delay time, high-to-low level output						58		58	ns	
	Low-level minimum pulse duration to switch RESET and $\overline{\text{RESET}}$	SENSE		V _{IH} = V _{IT+max} + 0.2 V, V _{IL} = V _{IT-min} - 0.2 V	3			4			μs
		$\overline{\text{RESIN}}$		V _{IL} = 0.2 × V _{DD} , V _{IH} = 0.7 × V _{DD}	1			1			
t _r	Rise time		RESET and $\overline{\text{RESET}}$	10% to 90%		8			8		ns/V
t _f	Fall time		RESET and $\overline{\text{RESET}}$	90% to 10%		4			4		

(1) NC = No capacitor, and includes up to 100-pF probe and jig capacitance.

PARAMETER MEASUREMENT INFORMATION



- A. For switching characteristics, $R_L = 2\text{ k}\Omega$
- B. $C_L = 50\text{ pF}$ includes jig and probe capacitance

Figure 2. RESET AND $\overline{\text{RESET}}$ Output Configurations

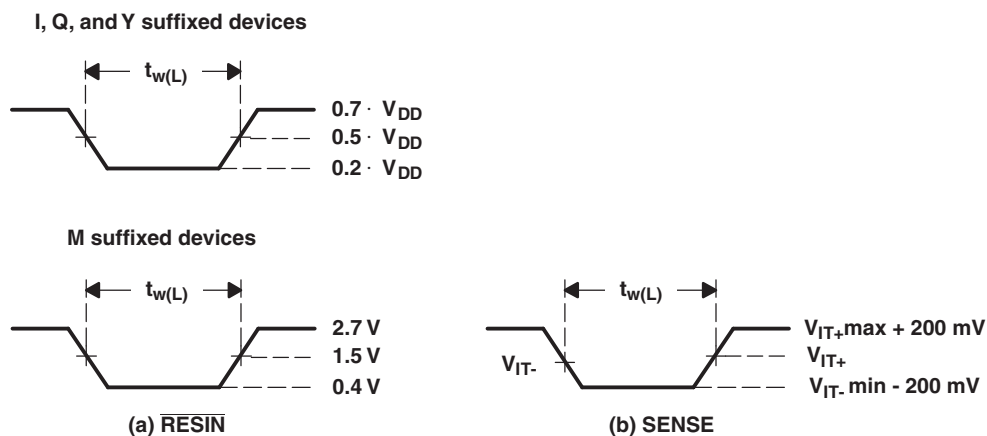


Figure 3. Input Pulse Definition Waveforms

TYPICAL CHARACTERISTICS

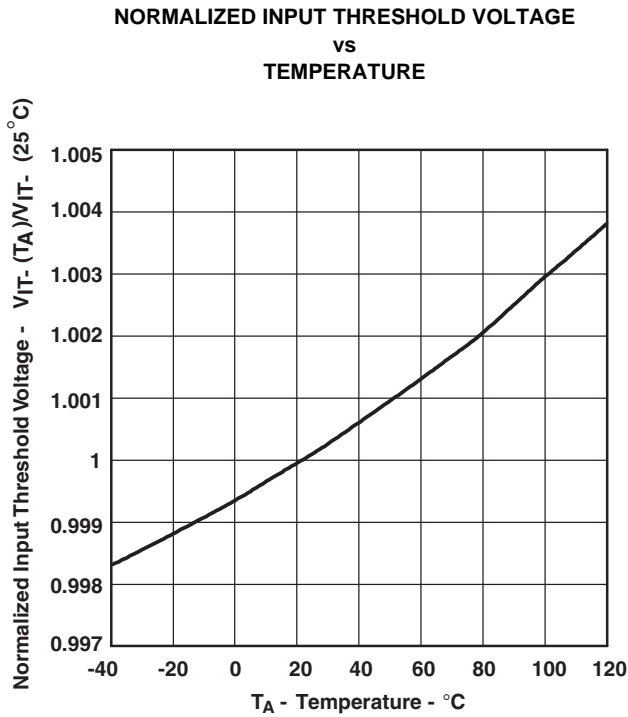


Figure 4.

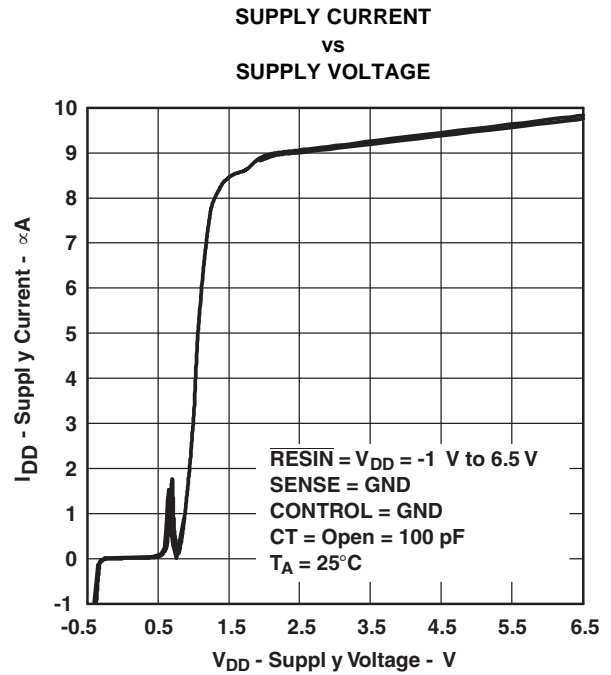


Figure 5.

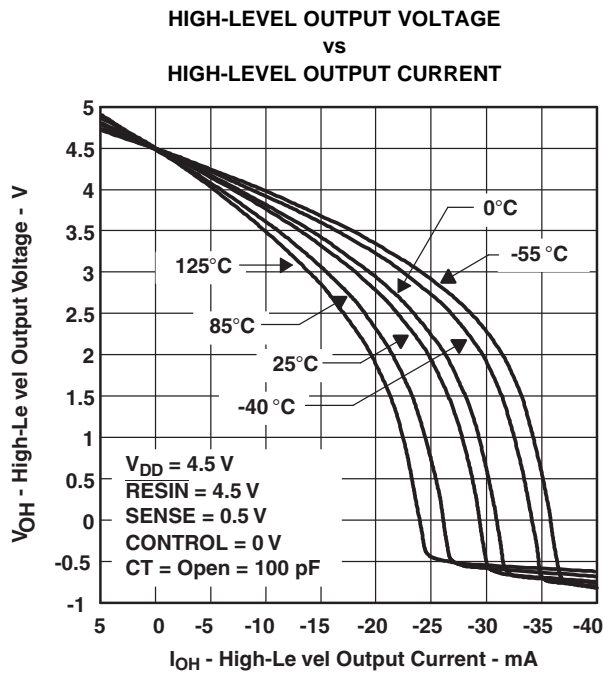


Figure 6.

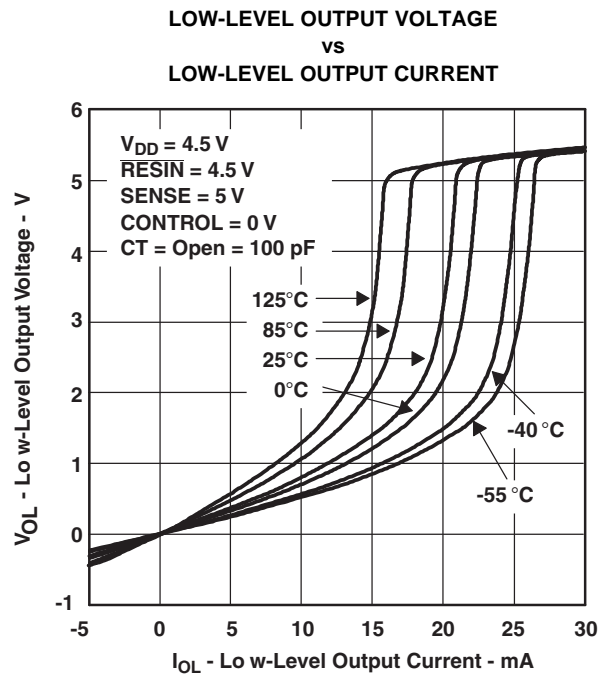
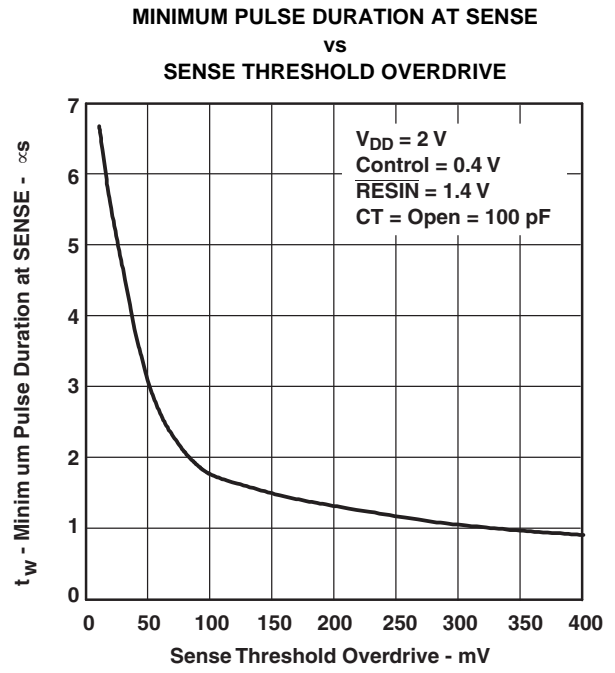
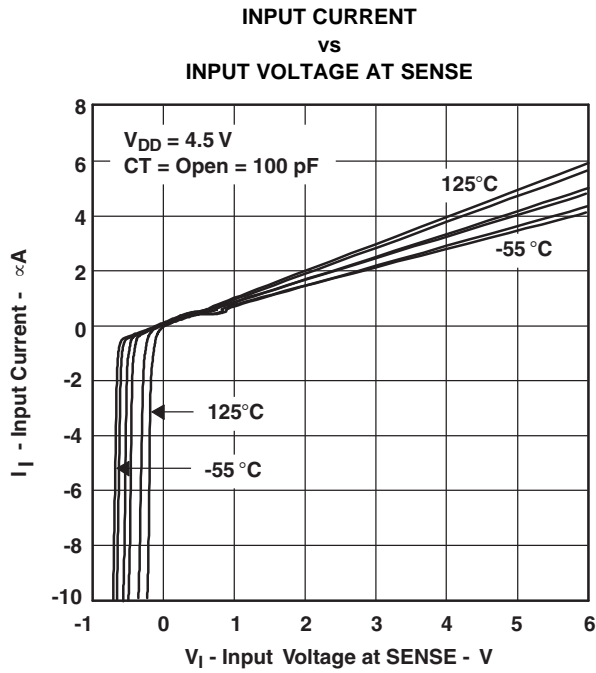


Figure 7.

TYPICAL CHARACTERISTICS (continued)



APPLICATION INFORMATION

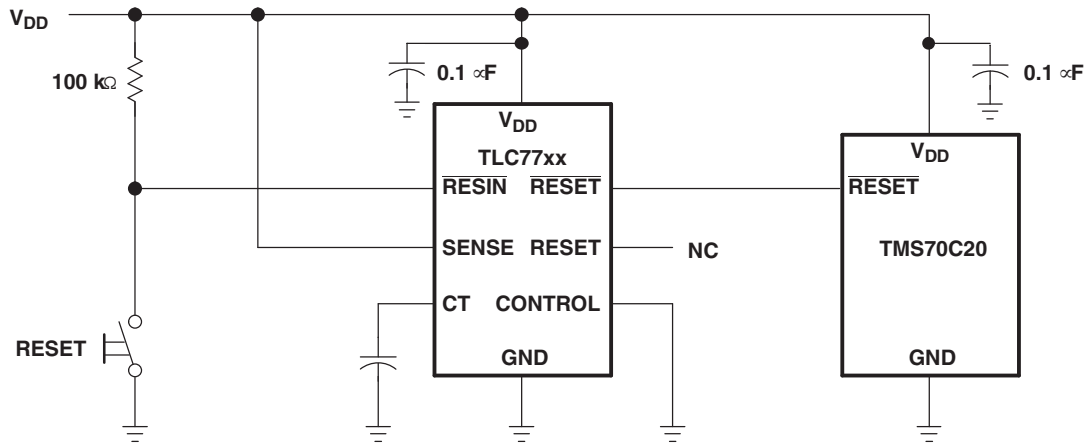


Figure 10. Reset Controller in a Microcomputer System

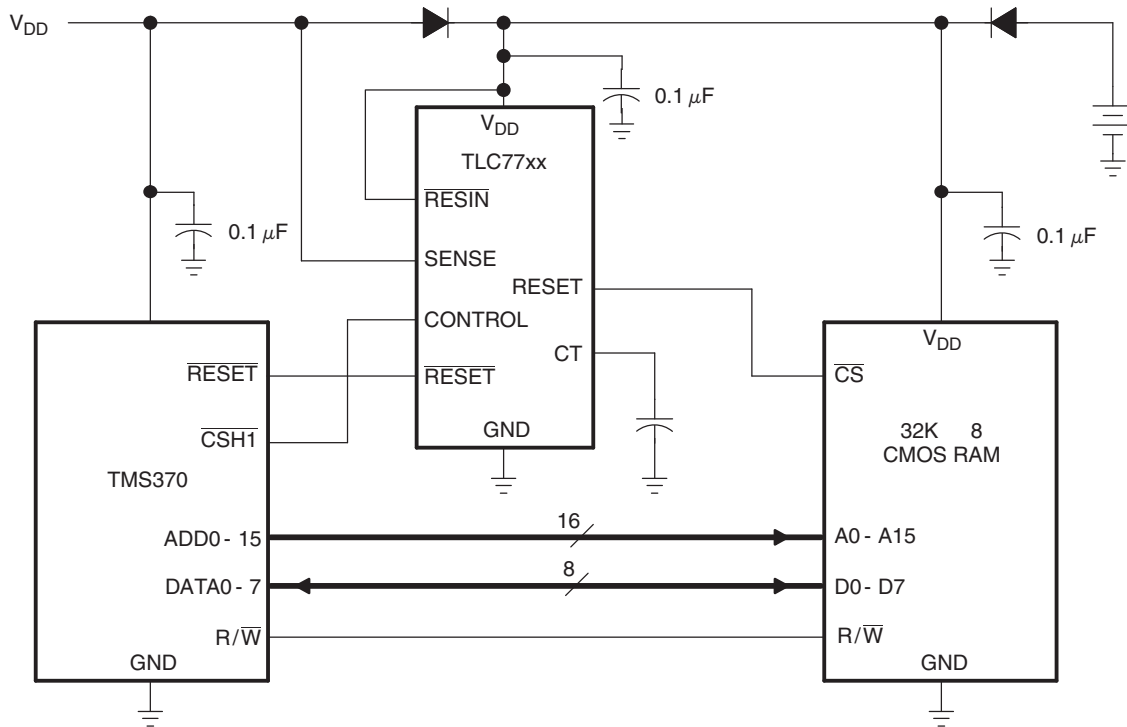


Figure 11. Data Retention During Power Down Using Static CMOS RAMs

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TLC7701MDREP	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7701MPWREP	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7701MPWREPG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7701QPWREP	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7705QPWREP	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7733MPWREP	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TLC7733MPWTEP	PREVIEW	TSSOP	PW	8	2000	TBD	Call TI	Call TI	
TLC7733QPWREP	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-01XE	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-02XE	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-03XE	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-04XE	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-04YE	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/04604-06XE	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

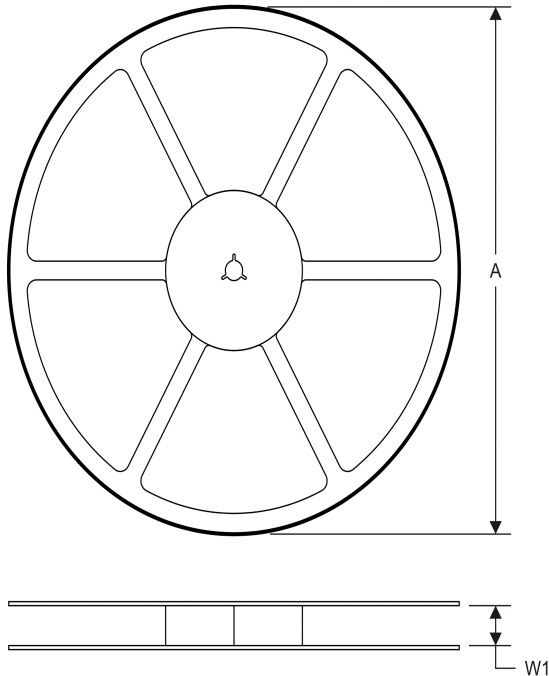
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF TLC7701-EP, TLC7705-EP, TLC7733-EP :

- Catalog: [TLC7701](#), [TLC7705](#), [TLC7733](#)
- Automotive: [TLC7701-Q1](#), [TLC7705-Q1](#), [TLC7733-Q1](#)
- Military: [TLC7705M](#), [TLC7733M](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Military - QML certified for Military and Defense Applications

TAPE AND REEL INFORMATION
REEL DIMENSIONS

TAPE DIMENSIONS


A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

TAPE AND REEL INFORMATION

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLC7701MDREP	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC7701MPWREP	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC7701QPWREP	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC7705QPWREP	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC7733MPWREP	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC7733QPWREP	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLC7701MDREP	SOIC	D	8	2500	367.0	367.0	35.0
TLC7701MPWREP	TSSOP	PW	8	2000	367.0	367.0	35.0
TLC7701QPWREP	TSSOP	PW	8	2000	367.0	367.0	35.0
TLC7705QPWREP	TSSOP	PW	8	2000	367.0	367.0	35.0
TLC7733MPWREP	TSSOP	PW	8	2000	367.0	367.0	35.0
TLC7733QPWREP	TSSOP	PW	8	2000	367.0	367.0	35.0

D (R-PDSO-G8)

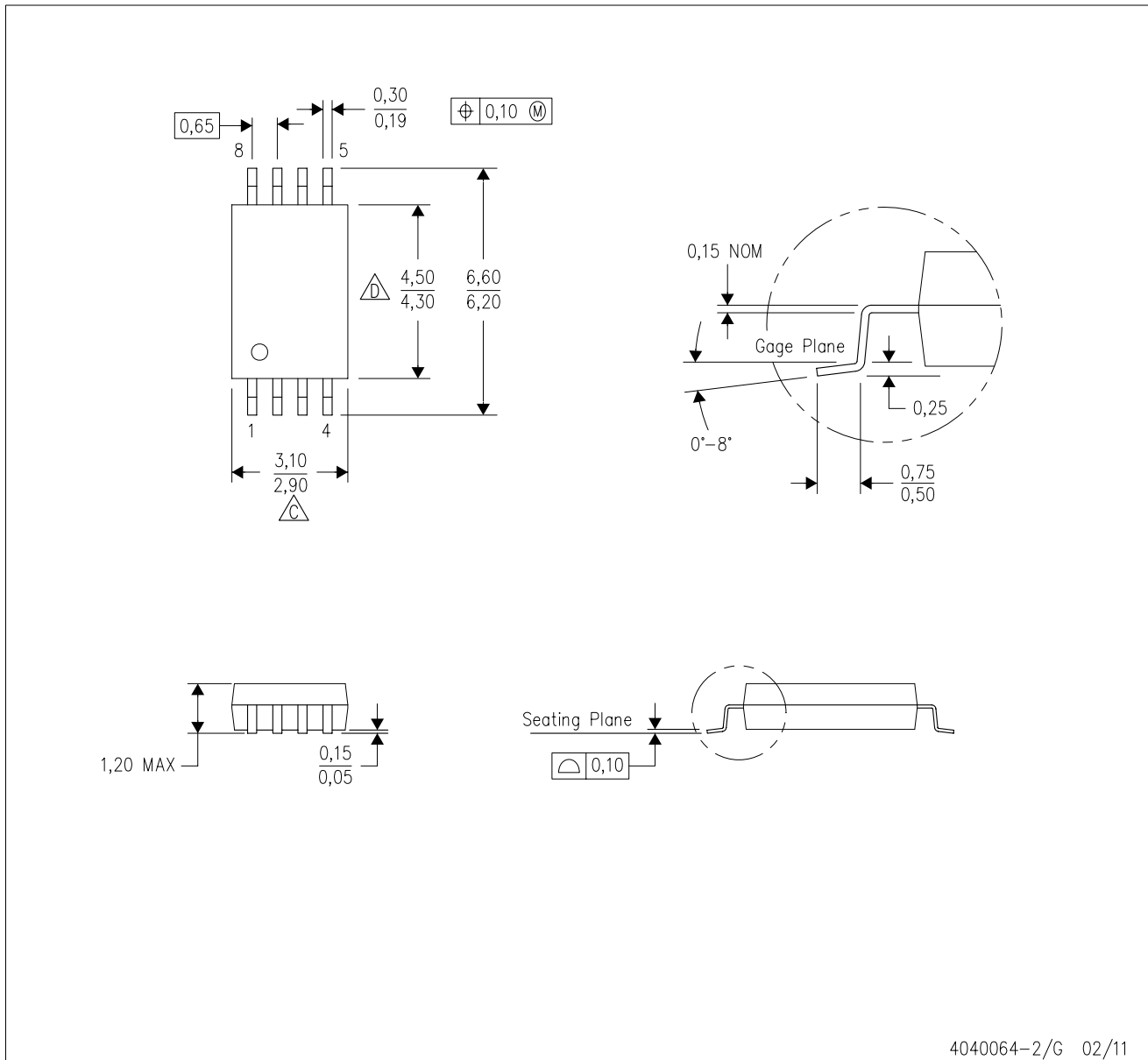
PLASTIC SMALL OUTLINE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Publication IPC-7351 is recommended for alternate designs.
 - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

PW (R-PDSO-G8)

PLASTIC SMALL OUTLINE



4040064-2/G 02/11

- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
 - B. This drawing is subject to change without notice.
 - C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
 - D. Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
 - E. Falls within JEDEC MO-153

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use. Components which have not been so designated are neither designed nor intended for automotive use; and TI will not be responsible for any failure of such components to meet such requirements.

Products

Audio	www.ti.com/audio
Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DLP® Products	www.dlp.com
DSP	dsp.ti.com
Clocks and Timers	www.ti.com/clocks
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
OMAP Applications Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

Applications

Automotive and Transportation	www.ti.com/automotive
Communications and Telecom	www.ti.com/communications
Computers and Peripherals	www.ti.com/computers
Consumer Electronics	www.ti.com/consumer-apps
Energy and Lighting	www.ti.com/energy
Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
Video and Imaging	www.ti.com/video

TI E2E Community

e2e.ti.com