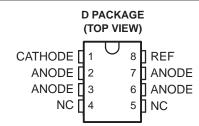
TL1431-Q1 PRECISION PROGRAMMABLE REFERENCE

SLVS534B - JUNE 2004 - REVISED APRIL 2008

- Qualified for Automotive Applications
- 0.4% Initial Voltage Tolerance
- 0.2-Ω Typical Output Impedance
- Fast Turnon . . . 500 ns
- Sink Current Capability . . . 1 mA to 100 mA
- Low Reference Current (REF)
- Adjustable Output Voltage . . . V_{I(ref)} to 36 V



NC – No internal connection ANODE terminals are connected internally.

description/ordering information

The TL1431 is a precision programmable reference with specified thermal stability over the automotive temperature range. The output voltage can be set to any value between $V_{I(ref)}$ (approximately 2.5 V) and 36 V with two external resistors (see Figure 16). This device has a typical output impedance of 0.2 Ω . Active output circuitry provides a very sharp turnon characteristic, making the device an excellent replacement for Zener diodes and other types of references in applications such as onboard regulation, adjustable power supplies, and switching power supplies.

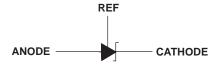
The TL1431Q is characterized for operation over the full automotive temperature range of -40°C to 125°C.

ORDERING INFORMATION†

TA	PACKAGE [‡]		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 125°C	SOIC (D)	Reel of 2500	TL1431QDRQ1	1431Q1

[†] For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at http://www.ti.com.

symbol



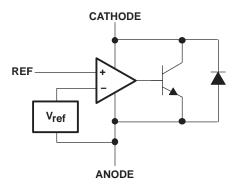


Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

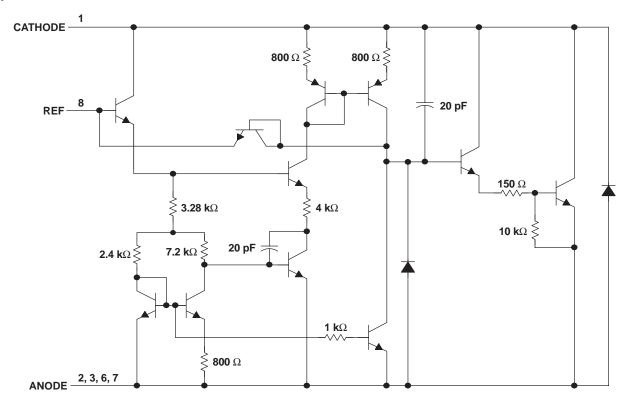


[‡] Package drawings, thermal data, and symbolization are available at http://www.ti.com/packaging.

functional block diagram



equivalent schematic†



† All component values are nominal.



TL1431-Q1 PRECISION PROGRAMMABLE REFERENCE

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Cathode voltage, V _{KA} (see Note 1)	37 V
Continuous cathode current range, I _{KA}	–100 mA to 150 mA
Reference input current range, I _{I(ref)}	
Package thermal impedance, θ _{JA} (see Notes 2 and 3)	
Operating virtual junction temperature, T _J	150°C
Continuous total power dissipation	See Dissipation Rating Table
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	
Storage temperature range, T _{stq}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values are with respect to ANODE, unless otherwise noted.
 - 2. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.
 - 3. The package thermal impedance is calculated in accordance with JESD 51-7.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{A}} \le 25^{\circ}\mbox{C}$ POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING	T _A = 125°C POWER RATING
D	1102 mW	10.3 mW/°C	638.5 mW	484 mW	72.1 mW

recommended operating conditions

		MIN	MAX	UNIT
VKA	Cathode voltage	V _{I(ref)}	36	V
IKA	Cathode current	1	100	mA
TA	Operating free-air temperature	-40	125	°C



TL1431-Q1 PRECISION PROGRAMMABLE REFERENCE

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electrical characteristics at specified free-air temperature, $I_{KA} = 10$ mA (unless otherwise noted)

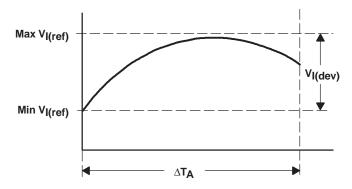
	PARAMETER	TEST CONDITIONS		T _A †	TEST CIRCUIT	MIN	TYP	MAX	UNIT
V Before a instantion		Vice - Vice o		25°C	Fig 4	2490	2500	2510	\/
V _{I(ref)}	Reference input voltage	$V_{KA} = V_{I(ref)}$		Full range	Figure 1	2470		2530	mV
V _{I(dev)}	Deviation of reference input voltage over full temperature range‡	$V_{KA} = V_{I(ref)}$		Full range	Figure 1		17	55	mV
$\frac{\Delta V_{I(ref)}}{\Delta V_{KA}}$	Ratio of change in reference input voltage to the change in cathode voltage	ΔV _K A = 3 V to 36 V		Full range	Figure 2		-1.1	-2	mV/V
	Defended innut suggest	D4 40 kg	DO	25°C	Figure 0		1.5	2.5	
I _{I(ref)}	Reference input current	$R1 = 10 \text{ k}\Omega$	R2 = ∞	Full range	Figure 2			4	μΑ
I _{I(dev)}	Deviation of reference input current over full temperature range‡	R1 = 10 kΩ,	R2 = ∞	Full range	Figure 2		0.5	2	μА
I _{min}	Minimum cathode current for regulation	V _{KA} = V _{I(ref)}		25°C	Figure 1		0.45	1	mA
	Off state and a decomposit	V 00.V		25°C	F: 0		0.18	0.5	4
loff	Off-state cathode current	$V_{KA} = 36 V,$	$V_{I(ref)} = 0$	Full range	Figure 3			2	μΑ
z _K A	Output impedance§	$V_{KA} = V_{I(ref)}$, $f \le 1$ kHz, $I_{KA} = 1$ mA to 100 mA		25°C	Figure 1		0.2	0.4	Ω

[†] Full range is -40°C to 125°C for Q-suffix devices.

$$\left|\alpha_{V_{I(ref)}}\right|\left(\frac{ppm}{^{\circ}C}\right) = \frac{\left(\frac{V_{I(dev)}}{V_{I(ref)} \text{ at } 25^{\circ}C}\right) \times 10^{6}}{\Delta T_{A}}$$

where

 $\Delta T_{\mbox{\scriptsize A}}$ is the rated operating temperature range of the device.



 $\alpha_{V_{l(ref)}}$ is positive or negative, depending on whether minimum $V_{l(ref)}$ or maximum $V_{l(ref)}$, respectively, occurs at the lower temperature.

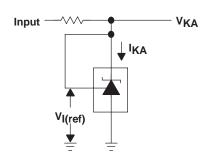
§ The output impedance is defined as: $|z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is given by: $|z'| = \frac{\Delta V}{\Delta I}$, which is approximately equal to $|z_{KA}| \left(1 + \frac{R1}{R2}\right)$.



[‡] The deviation parameters $V_{I(deV)}$ and $I_{I(deV)}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage $\alpha_{V_{I(ref)}}$ is defined as:

PARAMETER MEASUREMENT INFORMATION



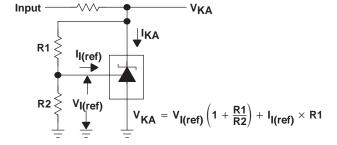


Figure 1. Test Circuit for $V_{(KA)} = V_{ref}$

Figure 2. Test Circuit for $V_{(KA)} > V_{ref}$

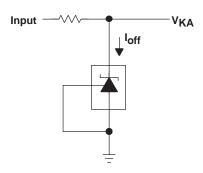


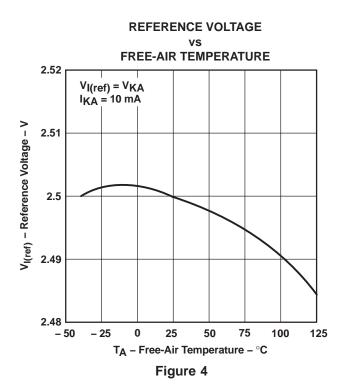
Figure 3. Test Circuit for Ioff

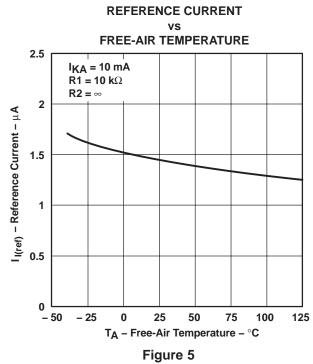
TYPICAL CHARACTERISTICS

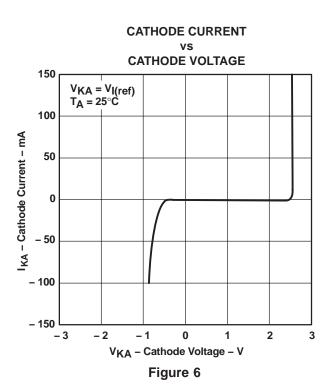
Table of Graphs

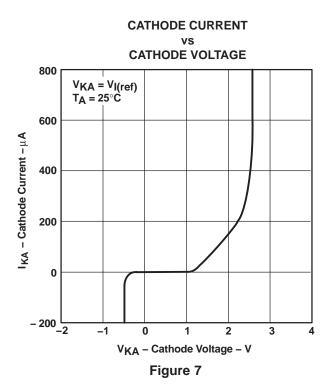
	FIGURE
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Small-signal voltage amplification vs Frequency	12
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TYPICAL CHARACTERISTICS†





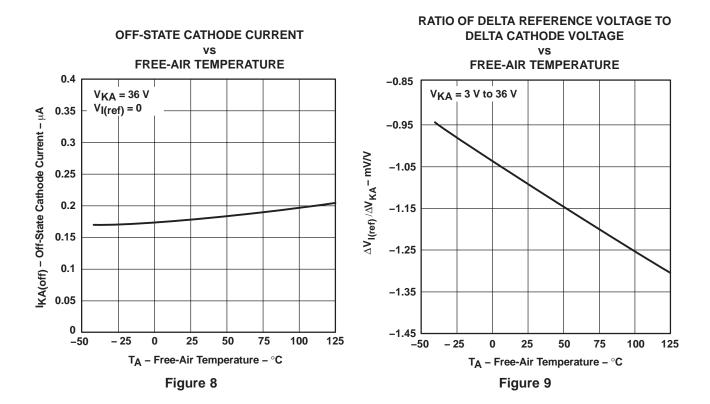




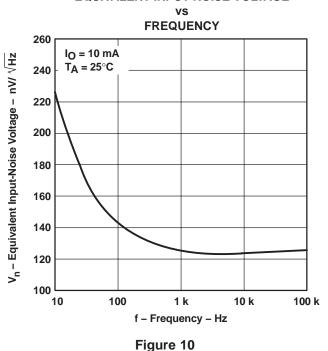
[†] Data at high and low temperatures are applicable only within the recommended operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS†



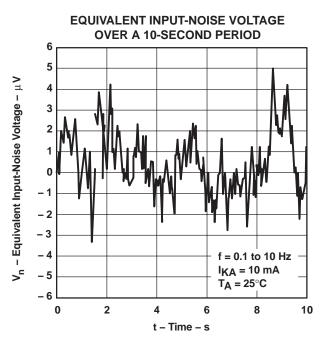
EQUIVALENT INPUT-NOISE VOLTAGE

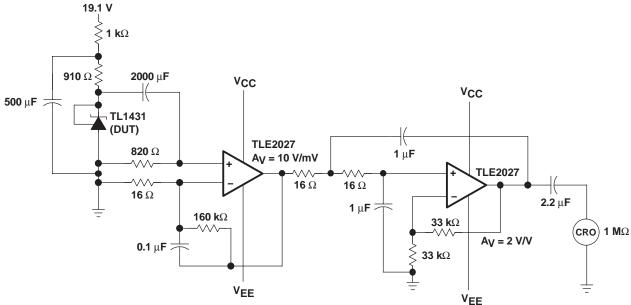


† Data at high and low temperatures are applicable only within the recommended operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS





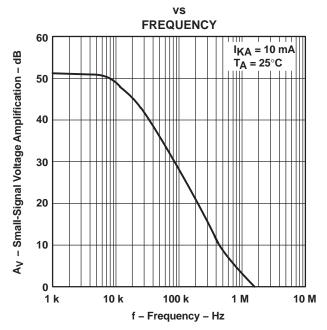
TEST CIRCUIT FOR 0.1-Hz TO 10-Hz EQUIVALENT INPUT-NOISE VOLTAGE

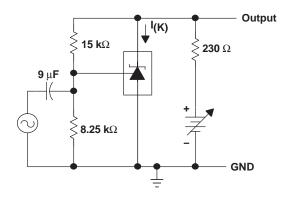
Figure 11



TYPICAL CHARACTERISTICS

SMALL-SIGNAL VOLTAGE AMPLIFICATION

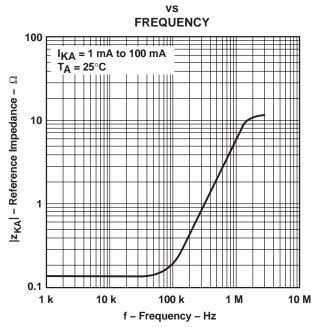


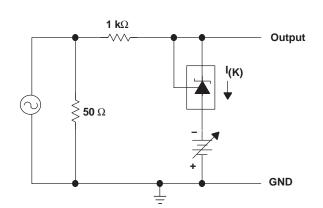


TEST CIRCUIT FOR VOLTAGE AMPLIFICATION

Figure 12

REFERENCE IMPEDANCE

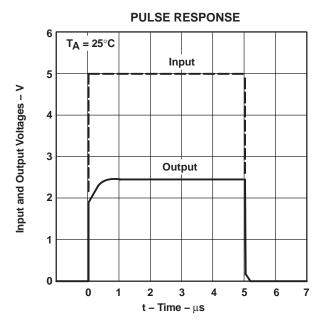




TEST CIRCUIT FOR REFERENCE IMPEDANCE

Figure 13

TYPICAL CHARACTERISTICS



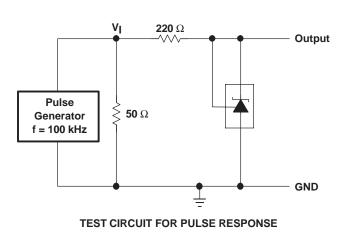
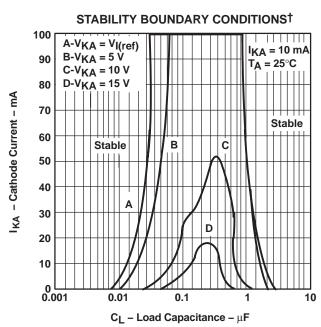
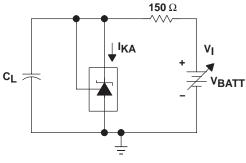


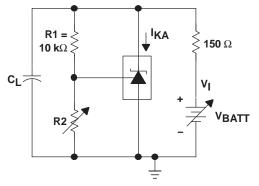
Figure 14



[†] The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ are adjusted to establish the initial V_{KA} and I_{KA} conditions, with C_L = 0. V_{BATT} and C_L then are adjusted to determine the ranges of stability.



TEST CIRCUIT FOR CURVE A



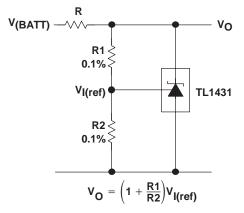
TEST CIRCUIT FOR CURVES B, C, AND D

Figure 15



Table of Application Circuits

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Crowbar	21
Precision 5-V, 1.5-A, 0.5% regulator	22
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Voltage monitor	25
Delay timer	26
Precision current limiter	27
Precision constant-current sink	28



NOTE A: R should provide cathode current \geq 1 mA to the TL1431 at minimum $V_{(BATT)}$.

Figure 16. Shunt Regulator

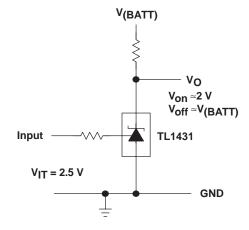
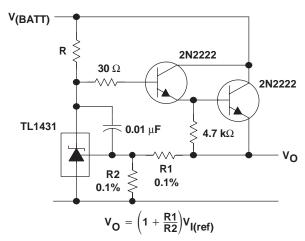


Figure 17. Single-Supply Comparator With Temperature-Compensated Threshold



NOTE A: R should provide cathode current ≥1 mA to the TL1431 at minimum V(BATT).

Figure 18. Precision High-Current Series Regulator

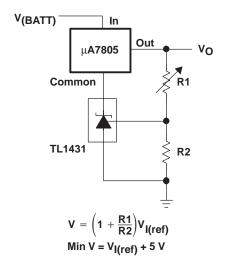


Figure 19. Output Control of a Three-Terminal Fixed Regulator

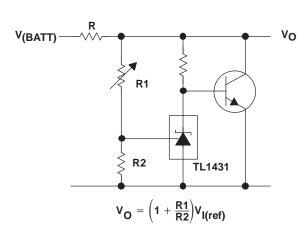
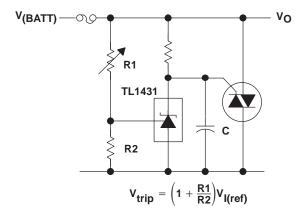
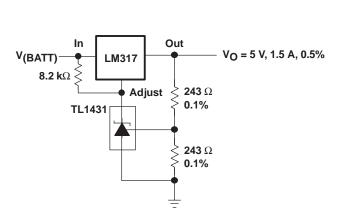


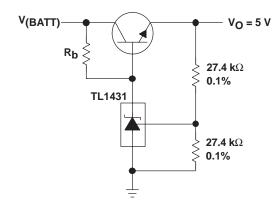
Figure 20. Higher-Current Shunt Regulator



NOTE A: Refer to the stability boundary conditions in Figure 15 to determine allowable values for C.

Figure 21. Crowbar





NOTE A: R_b should provide cathode current \geq 1 mA to the TL1431.

Figure 22. Precision 5-V, 1.5-A, 0.5% Regulator

Figure 23. 5-V Precision Regulator

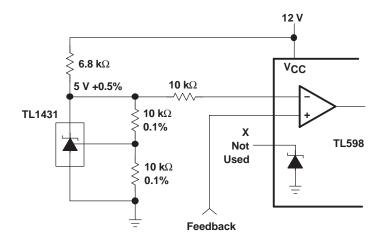
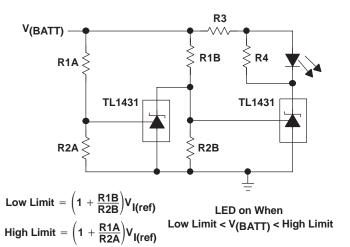


Figure 24. PWM Converter With 0.5% Reference



NOTE A: Select R3 and R4 to provide the desired LED intensity and cathode current ≥1 mA to the TL1431.

Figure 25. Voltage Monitor

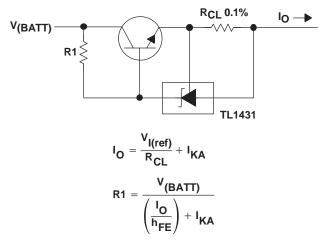


Figure 27. Precision Current Limiter

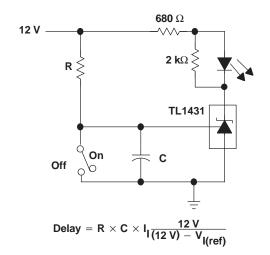


Figure 26. Delay Timer

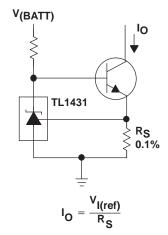


Figure 28. Precision Constant-Current Sink



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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TL1431QDRG4Q1	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
TL1431QDRQ1	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Request Free Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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OTHER QUALIFIED VERSIONS OF TL1431-Q1:

Catalog: TL1431

Enhanced Product: TL1431-EP





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Military: TL1431M

• Space: TL1431-SP

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Enhanced Product Supports Defense, Aerospace and Medical Applications
- Military QML certified for Military and Defense Applications
- Space Radiation tolerant, ceramic packaging and qualified for use in Space-based application

D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.



D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



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TI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use. Components which have not been so designated are neither designed nor intended for automotive use; and TI will not be responsible for any failure of such components to meet such requirements.

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