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LM19 2.4V, 10µA, TO-92 Temperature Sensor

Check for Samples: LM19

FEATURES

- Rated for Full -55°C to +130°C Range
- Available in a TO-92 Package
- Predictable Curvature Error
- Suitable for Remote Applications
- UL Recognized Component R

APPLICATIONS

- **Cellular Phones**
- Computers
- **Power Supply Modules**
- **Battery Management**
- FAX Machines
- **Printers**
- HVAC
- **Disk Drives**
- Appliances

KEY SPECIFICATIONS

- Accuracy at +30°C ±2.5 °C (max)
- Accuracy at +130°C & -55°C ±3.5 to ±3.8 °C (max)
- Power Supply Voltage Range +2.4V to +5.5V
- Current Drain 10 µA (max)
- Nonlinearity ±0.4 % (typ)
- Output Impedance 160 Ω (max)
- Load Regulation
 - $0\mu A < I_{L} < +16 \mu A$

DESCRIPTION

The LM19 is a precision analog output CMOS integrated-circuit temperature sensor that operates over a -55°C to +130°C temperature range. The power supply operating range is +2.4 V to +5.5 V. The transfer function of LM19 is predominately linear, yet has a slight predictable parabolic curvature. The accuracy of the LM19 when specified to a parabolic transfer function is ±2.5°C at an ambient temperature of +30°C. The temperature error increases linearly and reaches a maximum of ±3.8°C at the temperature range extremes. The temperature range is affected by the power supply voltage. At a power supply voltage of 2.7 V to 5.5 V the temperature range extremes are +130°C and -55°C. Decreasing the power supply voltage to 2.4 V changes the negative extreme to -30°C, while the positive remains at +130°C.

The LM19's quiescent current is less than 10 µA. Therefore, self-heating is less than 0.02°C in still air. Shutdown capability for the LM19 is intrinsic because its inherent low power consumption allows it to be powered directly from the output of many logic gates or does not necessitate shutdown at all.



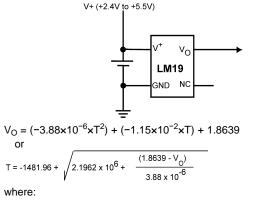
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Typical Application



T is temperature, and $V_{\rm O}$ is the measured output voltage of the LM19.

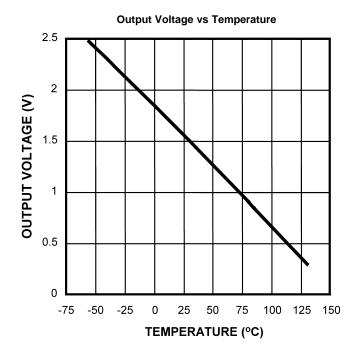


Figure 1. Full-Range Celsius (Centigrade) Temperature Sensor (−55°C to +130°C) Operating from a Single Li-Ion Battery Cell

Temperature (T)	Typical V _O
+130°C	+303 mV
+100°C	+675 mV
+80°C	+919 mV
+30°C	+1515 mV
+25°C	+1574 mV
0°C	+1863.9 mV
-30°C	+2205 mV
-40°C	+2318 mV
−55°C	+2485 mV

Connection Diagram



BOTTOM VIEW

Figure 2. TO-92 Package Number LP



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings⁽¹⁾

	Ratings				
Supply Voltage	+6.5V to -0.2V				
Output Voltage			(V ⁺ + 0.6 V) to −0.6 V		
Output Current			10 mA		
Input Current at any pin ⁽²⁾	Input Current at any pin ⁽²⁾				
Storage Temperature	-65°C to +150°C				
Maximum Junction Tempera	ture (T _{JMAX})		+150°C		
ESD Susceptibility ⁽³⁾	Human Body Model	2500 V			
	250 V				
Lead Temperature	TO-92 Package	Soldering (3 seconds dwell)	+240°C		

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The specified specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) When the input voltage (V₁) at any pin exceeds power supplies (V₁ < GND or V₁ > V⁺), the current at that pin should be limited to 5 mA.
 (3) The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

Operating Ratings⁽¹⁾

Specified Temperature Range		T _{MIN} ≤ T _A ≤ T _{MAX}
$2.4 \text{ V} \le \text{V}^+ \le 2.7 \text{ V}$	−30°C ≤ T _A ≤ +130°C	
2.7 V ≤ V ⁺ ≤ 5.5 V	−55°C ≤ T _A ≤ +130°C	
Supply Voltage Range (V ⁺)	+2.4 V to +5.5 V	
Thermal Resistance, $\theta_{JA}^{(2)}$	TO-92	150°C/W

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The specified specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) The junction to ambient thermal resistance (θ_{JA}) is specified without a heat sink in still air.

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Electrical Characteristics

Unless otherwise noted, these specifications apply for V⁺ = +2.7 V_{DC}. Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$; Unless otherwise noted.

Parameter	Conditions	Typical ⁽¹⁾	LM19C	Units
			Limits ⁽²⁾	(Limit)
Temperature to Voltage Error	$T_A = +25^{\circ}C \text{ to } +30^{\circ}C$		±2.5	°C (max)
$V_{O} = (-3.88 \times 10^{-6} \times T^{2})$ + $(-1.15 \times 10^{-2} \times T)$ + $1.8639 V^{(3)}$	T _A = +130°C		±3.5	°C (max)
+(1.13×10 ×1)+1.0039	T _A = +125°C		±3.5	°C (max)
	$T_{A} = +100^{\circ}C$		±3.2	°C (max)
	T _A = +85°C		±3.1	°C (max)
	$T_A = +80^{\circ}C$		±3.0	°C (max)
	$T_A = 0^{\circ}C$		±2.9	°C (max)
	$T_A = -30^{\circ}C$		±3.3	°C (min)
	$T_A = -40^{\circ}C$		±3.5	°C (max)
	T _A = −55°C		±3.8	°C (max)
Output Voltage at 0°C		+1.8639		V
Variance from Curve		±1.0		°C
Non-Linearity ⁽⁴⁾	$-20^{\circ}C \le T_A \le +80^{\circ}C$	±0.4		%
Sensor Gain (Temperature Sensitivity or Average Slope) to equation: V _O =-11.77 mV/°CxT+1.860V	$-30^{\circ}C \le T_A \le +100^{\circ}C$	-11.77	-11.0 -12.6	mV/°C (min) mV/°C (max)
Output Impedance	$0 \ \mu A \le I_L \le +16 \ \mu A^{(5)(6)}$		160	Ω (max)
Load Regulation ⁽⁷⁾	$0 \ \mu A \le I_L \le +16 \ \mu A^{(5)(6)}$		-2.5	mV (max)
Line Regulation ⁽⁸⁾	+2. 4 V ≤ V ⁺ ≤ +5.0V		+3.7	mV/V (max)
	$+5.0 V \le V^+ \le +5.5 V$		+11	mV (max)
Quiescent Current	+2. 4 V ≤ V ⁺ ≤ +5.0V	4.5	7	μA (max)
	$+5.0V \leq V^+ \leq +5.5V$	4.5	9	μA (max)
	+2. 4 V ≤ V ⁺ ≤ +5.0V	4.5	10	μA (max)
Change of Quiescent Current	+2. 4 V ≤ V ⁺ ≤ +5.5V	+0.7		μA
Temperature Coefficient of Quiescent Current		-11		nA/°C
Shutdown Current	V ⁺ ≤ +0.8 V	0.02		μA

(1) Typicals are at $T_J = T_A = 25^{\circ}C$ and represent most likely parametric norm.

Limits are ensured to AOQL (Average Outgoing Quality Level). (2)

(3)Accuracy is defined as the error between the measured and calculated output voltage at the specified conditions of voltage, current, and temperature (expressed in°C).

Non-Linearity is defined as the deviation of the calculated output-voltage-versus-temperature curve from the best-fit straight line, over (4)the temperature range specified.

Negative currents are flowing into the LM19. Positive currents are flowing out of the LM19. Using this convention the LM19 can at most (5) sink $-1 \ \mu A$ and source $+16 \ \mu A$.

(6)

Load regulation or output impedance specifications apply over the supply voltage range of +2.4V to +5.5V. Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating (7) effects can be computed by multiplying the internal dissipation by the thermal resistance.

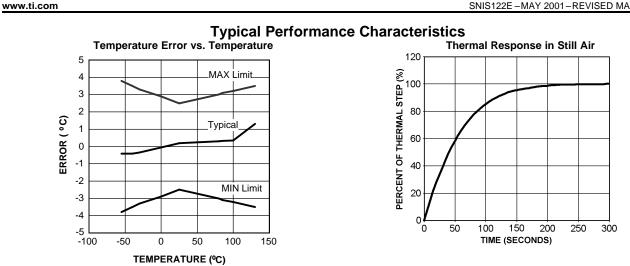
Line regulation is calculated by subtracting the output voltage at the highest supply input voltage from the output voltage at the lowest (8)supply input voltage.



(1)

(3)

(5)



LM19 TRANSFER FUNCTION

The LM19's transfer function can be described in different ways with varying levels of precision. A simple linear transfer function, with good accuracy near 25°C, is

Over the full operating temperature range of -55°C to +130°C, best accuracy can be obtained by using the parabolic transfer function

$$V_{\rm O} = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639$$
⁽²⁾

solving for T:

T = -1481.96 +
$$\sqrt{2.1962 \times 10^6 + \frac{(1.8639 - V_0)}{3.88 \times 10^{-6}}}$$

A linear transfer function can be used over a limited temperature range by calculating a slope and offset that give best results over that range. A linear transfer function can be calculated from the parabolic transfer function of the LM19. The slope of the linear transfer function can be calculated using the following equation:

 $m = -7.76 \times 10^{-6} \times T - 0.0115$

where

• T is the middle of the temperature range of interest and m is in V/°C. (4)

For example for the temperature range of $T_{min} = -30$ to $T_{max} = +100$ °C:

 $T = 35^{\circ}C$

and

m = -11.77 mV/°C

The offset of the linear transfer function can be calculated using the following equation:

 $b = (V_{OP}(T_{max}) + V_{OP}(T) - m \times (T_{max}+T))/2$

where

- $V_{OP}(T_{max})$ is the calculated output voltage at T_{max} using the parabolic transfer function for V_{O} .
- V_{OP}(T) is the calculated output voltage at T using the parabolic transfer function for V_O.

Using this procedure the best fit linear transfer function for many popular temperature ranges was calculated in Table 1. As shown in Table 1 the error that is introduced by the linear transfer function increases with wider temperature ranges.

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(6)

Table 1. First Order Equations Optimized For Different Temperature Ranges								
Temperat	ure Range	Linear Equation	Maximum Deviation of Linear Equation from					
T _{min} (°C)	T _{max} (°C)	V _O =	Parabolic Equation (°C)					
-55	+130	−11.79 mV/°C × T + 1.8528 V	±1.41					
-40	+110	−11.77 mV/°C × T + 1.8577 V	±0.93					
-30	+100	−11.77 mV/°C × T + 1.8605 V	±0.70					
-40	+85	−11.67 mV/°C × T + 1.8583 V	±0.65					
-10	+65	−11.71 mV/°C × T + 1.8641 V	±0.23					
+35	+45	−11.81 mV/°C × T + 1.8701 V	±0.004					
+20	+30	−11.69 mV/°C × T + 1.8663 V	±0.004					

Table 1. First Order Equations Optimized For Different Temperature Ranges

Mounting

The LM19 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface. The temperature that the LM19 is sensing will be within about +0.02°C of the surface temperature to which the LM19's leads are attached.

This presumes that the ambient air temperature is almost the same as the surface temperature; if the air temperature were much higher or lower than the surface temperature, the actual temperature measured would be at an intermediate temperature between the surface temperature and the air temperature.

To ensure good thermal conductivity the backside of the LM19 die is directly attached to the GND pin. The tempertures of the lands and traces to the other leads of the LM19 will also affect the temperature that is being sensed.

Alternatively, the LM19 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LM19 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as Humiseal and epoxy paints or dips are often used to ensure that moisture cannot corrode the LM19 or its connections.

The thermal resistance junction to ambient (θ_{JA}) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. For the LM19 the equation used to calculate the rise in the die temperature is as follows:

$$\mathsf{T}_\mathsf{J} = \mathsf{T}_\mathsf{A} + \boldsymbol{\theta}_\mathsf{J}_\mathsf{A} \left[(\mathsf{V}^+ \: \mathsf{I}_\mathsf{Q}) + (\mathsf{V}^+ - \mathsf{V}_\mathsf{O}) \: \mathsf{I}_\mathsf{L} \right]$$

150

TBD

where

Still air

Moving air

I_Q is the quiescent current and I_L is the load current on the output.

Since the LM19's junction temperature is the actual temperature being measured care should be taken to minimize the load current that the LM19 is required to drive.

Table 2 summarizes the rise in die temperature of the LM19 without any loading, and the thermal resistance for different conditions.

		g and monthly not	(UJA)	
Т	D-92	TO-92 small heat fin		
no he	eat sink			
θ _{JA}	T _J – T _A	θ _{JA}	T _J – T _A	
(°C/W)	(°C)	(°C/W)	(°C)	

TBD

TBD

TBD

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TBD

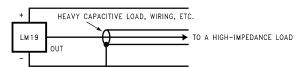


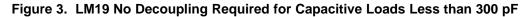
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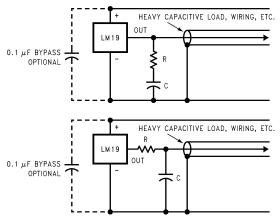
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Capacitive Loads

The LM19 handles capacitive loading well. Without any precautions, the LM19 can drive any capacitive load less than 300 pF as shown in Figure 3. Over the specified temperature range the LM19 has a maximum output impedance of 160 Ω . In an extremely noisy environment it may be necessary to add some filtering to minimize noise pickup. It is recommended that 0.1 µF be added from V⁺ to GND to bypass the power supply voltage, as shown in Figure 4. In a noisy environment it may even be necessary to add a capacitor from the output to ground with a series resistor as shown in Figure 4. A 1 µF output capacitor with the 160 Ω maximum output impedance and a 200 Ω series resistor will form a 442 Hz lowpass filter. Since the thermal time constant of the LM19 is much slower, the overall response time of the LM19 will not be significantly affected.







Either placement of resistor as shown above is just as effective.

Figure 4. LM19 with Filter for Noisy Environment and Capacitive Loading greater than 300 pF

Table 3. LM19 with Filter for Noisy Environment and Capacitive Loading greater than 300 pF

R (Ω)	C (µF)
200	1
470	0.1
680	0.01
1 k	0.001

TEXAS INSTRUMENTS

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Applications Circuits

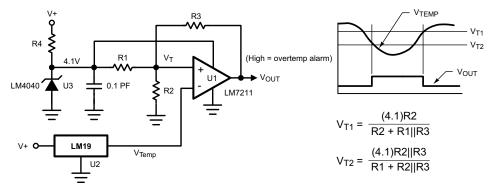
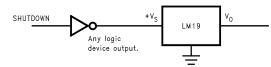


Figure 5. Centigrade Thermostat





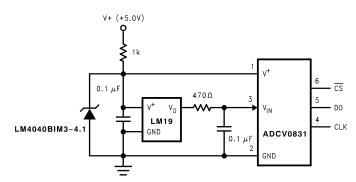


Figure 7. Suggested Connection to a Sampling Analog to Digital Converter Input Stage

Most CMOS ADCs found in ASICs have a sampled data comparator input structure that is notorious for causing grief to analog output devices such as the LM19 and many op amps. The cause of this grief is the requirement of instantaneous charge of the input sampling capacitor in the ADC. This requirement is easily accommodated by the addition of a capacitor. Since not all ADCs have identical input stages, the charge requirements will vary necessitating a different value of compensating capacitor. This ADC is shown as an example only. If a digital output temperature is required please refer to devices such as the LM74.

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Changes from	Revision D) (March	2013) to	Revision F	
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•	Changed layout of National Data Sheet to TI format	8
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11-Apr-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type	•	Pins	•	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
	(1)		Drawing		Qty	(2)		(3)		(4)	
LM19CIZ/LFT4	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	SNCU	Level-1-NA-UNLIM		LM19 CIZ	Samples
LM19CIZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	SNCU	Level-1-NA-UNLIM	-55 to 130	LM19 CIZ	Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between

the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

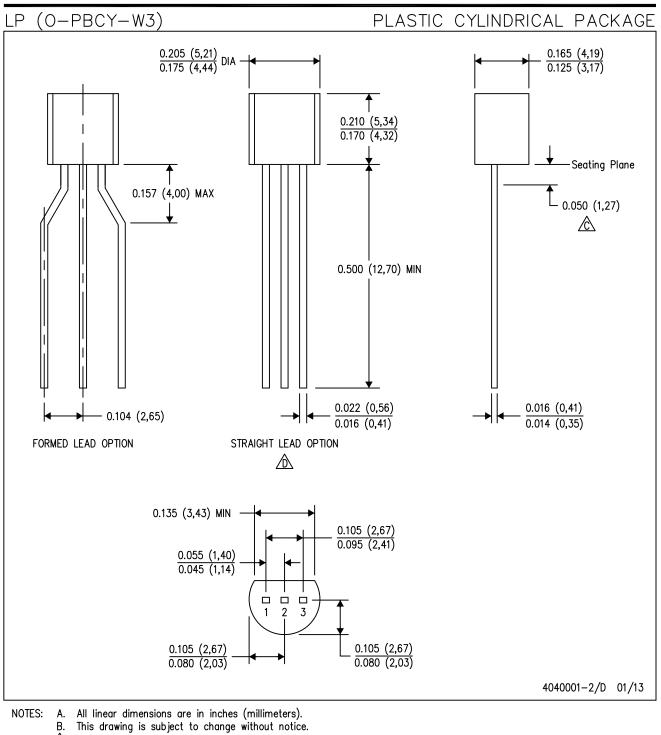
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

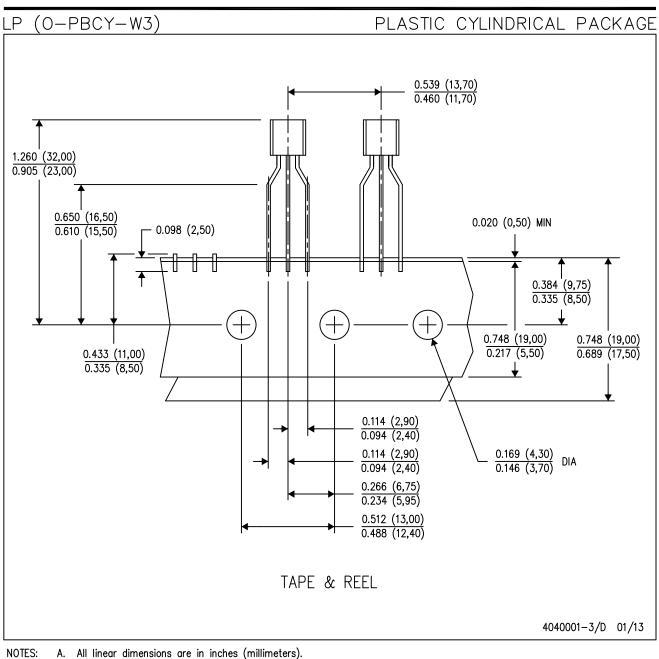
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- 🖄 Lead dimensions are not controlled within this area.
- $\overline{\bigtriangleup}$ Falls within JEDEC TO-226 Variation AA (TO-226 replaces TO-92).
- E. Shipping Method:
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 - Formed lead option available in tape & reel or ammo pack.
 - Specific products can be offered in limited combinations of shipping mediums and lead options.
 - Consult product folder for more information on available options.





Α.

B. This drawing is subject to change without notice.

C. Tape and Reel information for the Formed Lead Option package.



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