54ACQ244 • 54ACTQ244 Quiet Series Octal Buffer/Line Driver with TRI-STATE Outputs

Check for Samples: 54ACQ244, 54ACTQ244

FEATURES

- **I_{CC} and I_{OZ} Reduced by 50%**
- **Ensured Simultaneous Switching Noise Level and Dynamic Threshold Performance**
- **Improved Latch-Up Immunity**
- **TRI-STATE Outputs Drive Bus Lines or Buffer Memory Address Registers**
- **Outputs Source/Sink 24 mA**
- **Faster Prop Delays than the Standard 'AC/'ACT244**
- **4 kV Minimum ESD Immunity**
- **Standard Microcircuit Drawing (SMD)**
  - **'ACTQ244: 5962-92186**
  - **'ACQ244: 5962-92176**

DESCRIPTION

The 'ACQ/'ACTQ244 is an octal buffer and line driver designed to be employed as a memory address driver, clock driver and bus oriented transmitter or receiver which provides improved PC board density. The ACQ/ACTQ utilizes TI Quiet Series technology to ensure quiet output switching and improved dynamic threshold performance. FACT Quiet Series™ features GTO™ output control and undershoot corrector in addition to a split ground bus for superior performance.

Logic Symbol

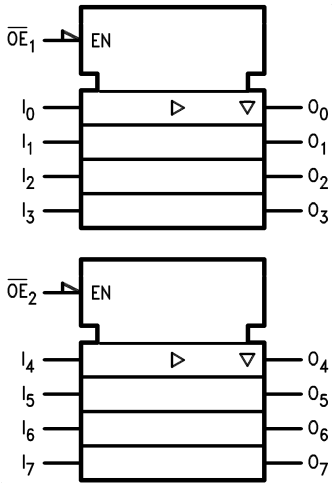

Figure 1. IEE/IEC

Pin Names	Description
$\overline{OE}_1$, $\overline{OE}_2$	TRI-STATE Output Enable Inputs
I_0–I_7	Inputs
O_0–O_7	Outputs

Connection Diagrams

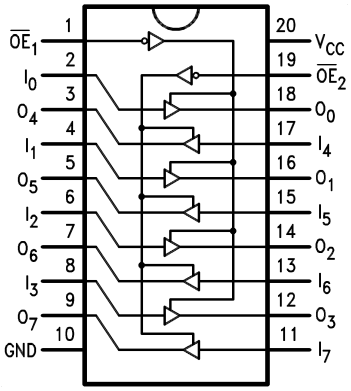

Figure 2. Pin Assignment for CDIP and CLGA

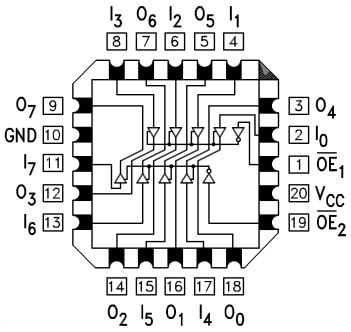

Figure 3. Pin Assignment for LCCC

Truth Table

Inputs		Outputs
$\overline{OE}_1$	I_n	(Pins 12, 14, 16, 18)
L	L	L
L	H	H
H	X	Z
Inputs		**Outputs**
$\overline{OE}_2$	I_n	(Pins 3, 5, 7, 9)
L	L	L
L	H	H
H	X	Z

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial
Z = High Impedance

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings(1)(2)

Supply Voltage (V_{CC})		−0.5V to +7.0V
DC Input Diode Current (I_{IK})	V_I = −0.5V	−20 mA
	V_I = V_{CC} + 0.5V	+20 mA
DC Input Voltage (V_I)		−0.5V to V_{CC} + 0.5V
DC Output Diode Current (I_{OK})	V_O = −0.5V	−20 mA
	V_O = V_{CC} + 0.5V	+20 mA
DC Output Voltage (V_O)		−0.5V to V_{CC} + 0.5V
DC Output Source or Sink Current (I_O)		±50 mA
DC V_{CC} or Ground Current per Output Pin (I_{CC} or I_{GND})		±50 mA
Storage Temperature (T_{STG})		−65°C to +150°C
DC Latch-Up Source or Sink Current		±300 mA
Junction Temperature (T_J)	CDIP	175°C

(1) Absolute Maximum Ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. TI does not recommend operation of FACT® circuits outside databook specifications.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

Recommended Operating Conditions

Supply Voltage (V_{CC})	'ACQ	2.0V to 6.0V
	'ACTQ	4.5V to 5.5V
Input Voltage (V_I)		0V to V_{CC}
Output Voltage (V_O)		0V to V_{CC}
Operating Temperature (T_A)	54ACQ/ACTQ	−55°C to +125°C
Minimum Input Edge Rate ΔV/Δt 'ACQ Devices	V_{IN} from 30% to 70% of V_{CC} V_{CC} @ 3.0V, 4.5V, 5.5V	125 mV/ns
Minimum Input Edge Rate ΔV/Δt 'ACTQ Devices	V_{IN} from 0.8V to 2.0V V_{CC} @ 4.5V, 5.5V	125 mV/ns

DC Electrical Characteristics for 'ACQ Family Devices

Symbol	Parameter	V_{CC} (V)	54ACQ T_A = −55°C to +125°C Ensured Limits	Units	Conditions
V_{IH}	Minimum High Level Input Voltage	3.0	2.1	V	V_{OUT} = 0.1V or V_{CC} − 0.1V
		4.5	3.15		
		5.5	3.85		
V_{IL}	Maximum Low Level Input Voltage	3.0	0.9	V	V_{OUT} = 0.1V or V_{CC} − 0.1V
		4.5	1.35		
		5.5	1.65		
V_{OH}	Minimum High Level Output Voltage	3.0	2.9	V	I_{OUT} = −50 μA
		4.5	4.4		
		5.5	5.4		
					See(1) V_{IN} = V_{IL} or V_{IH}
		3.0	2.4		I_{OH} = −12 mA
		4.5	3.7	V	I_{OH} = −24 mA
		5.5	4.7		I_{OH} = −24 mA
V_{OL}	Maximum Low Level Output Voltage	3.0	0.1	V	I_{OUT} = 50 μA
		4.5	0.1		
		5.5	0.1		
					See(1) V_{IN} = V_{IL} or V_{IH}
		3.0	0.50		I_{OL} = 12 mA
		4.5	0.50	V	I_{OL} = 24 mA
		5.5	0.50		I_{OL} = 24 mA
I_{IN}	Maximum Input Leakage Current	5.5	±1.0	μA	V_I = V_{CC}, GND(2)
I_{OLD}	Minimum Dynamic Output Current(3)	5.5	50	mA	V_{OLD} = 1.65V Max
I_{OHD}		5.5	−50	mA	V_{OHD} = 3.85V Min
I_{CC}	Maximum Quiescent Supply Current	5.5	80.0	μA	V_{IN} = V_{CC} or GND(2)
I_{OZ}	Maximum TRI-STATE Leakage Current	5.5	±5.0	μA	V_I(OE) = V_{IL}, V_{IH} V_I = V_{CC}, GND V_O = V_{CC}, GND
V_{OLP}	Quiet Output Maximum Dynamic V_{OL}	5.0	1.5	V	See(4)(5)
V_{OLV}	Quiet Output Minimum Dynamic V_{OL}	5.0	−1.2	V	See(4)(5)

(1) All outputs loaded thresholds on input associated with output under test.
(2) I_{IN} and I_{CC} @ 3.0V are ensured to be less than or equal to the respective limit @ 5.5V V_{CC}.I_{CC} for 54ACQ @ 25°C is identical to 74ACQ @ 25°C.
(3) Maximum test duration 2.0 ms, one output loaded at a time.
(4) PDIP package.
(5) Max number of outputs defined as (n). Data Inputs are driven 0V to 5V. One output @ GND.

DC Electrical Characteristics for 'ACTQ Family Devices

Symbol	Parameter	V_{CC} (V)	54ACTQ −55°C to +125°C Ensured Limits	Units	Conditions
V_{IH}	Minimum High Level Input Voltage	4.5	2.0	V	V_{OUT} = 0.1V
		5.5	2.0		or V_{CC} − 0.1V
V_{IL}	Maximum Low Level Input Voltage	4.5	0.8	V	V_{OUT} = 0.1V
		5.5	0.8		or V_{CC} − 0.1V
V_{OH}	Minimum High Level Output Voltage	4.5	4.4	V	I_{OUT} = −50 µA
		5.5	5.4		
					See(1) V_{IN} = V_{IL} or V_{IH}
		4.5	3.70	V	I_{OH} = −24 mA
		5.5	4.70		I_{OH} = −24 mA
V_{OL}	Maximum Low Level Output Voltage	4.5	0.1	V	I_{OUT} = 50 µA
		5.5	0.1		
					See(1) V_{IN} = V_{IL} or V_{IH}
		4.5	0.50	V	I_{OL} = 24 mA
		5.5	0.50		I_{OL} = 24 mA
I_{IN}	Maximum Input Leakage Current	5.5	±1.0	µA	V_I = V_{CC}, GND
I_{OZ}	Maximum TRI-STATE Leakage Current	5.5	±5.0	µA	V_I = V_{IL}, V_{IH} V_O = V_{CC}, GND
I_{CCT}	Maximum I_{CC}/Input	5.5	1.6	mA	V_I = V_{CC} − 2.1V
I_{OLD}	Minimum Dynamic(2) Output Current	5.5	50	mA	V_{OLD} = 1.65V Max
I_{OHD}		5.5	−50	mA	V_{OHD} = 3.85V Min
I_{CC}	Maximum Quiescent Supply Current	5.5	80.0	µA	V_{IN} = V_{CC} or GND(3)
V_{OLP}	Quiet Output Maximum Dynamic V_{OL}	5.0	1.5	V	See(4) (5)
V_{OLV}	Quiet Output Minimum Dynamic V_{OL}	5.0	−1.2	V	See(4) (5)

(1) All outputs loaded thresholds on input associated with output under test.
(2) Maximum test duration 2.0 ms, one output loaded at a time.
(3) I_{CC} for 54ACTQ @ 25°C is identical to 74ACTQ @ 25°C.
(4) PDIP package.
(5) Max number of outputs defined as (n). Data Inputs are driven 0V to 3V. One output @ GND.

AC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)[1]	54ACQ T_A = −55°C to +125°C C_L = 50 pF		Units	Fig. No.
			Min	Max		
t_{PHL}, t_{PLH}	Propagation Delay	3.3	1.0	12.5	ns	
	Data to Output	5.0	1.0	9.0		
t_{PZL}, t_{PZH}	Output Enable Time	3.3	1.0	12.0	ns	
		5.0	1.0	10.0		
t_{PHZ}, t_{PLZ}	Output Disable Time	3.3	1.0	11.5	ns	
		5.0	1.0	10.0		

(1) Voltage Range 5.0 is 5.0V ±0.5V.
Voltage Range 3.3 is 3.3V ±0.3V.

AC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)[1]	54ACTQ T_A = −55°C to +125°C C_L = 50 pF		Units	Fig. No.
			Min	Max		
t_{PHL}, t_{PLH}	Propagation Delay	5.0	1.5	9.0	ns	
	Data to Output					
t_{PZL}, t_{PZH}	Output Enable Time	5.0	1.5	10.5	ns	
t_{PHZ}, t_{PLZ}	Output Disable Time	5.0	1.5	10.5	ns	

(1) Voltage Range 5.0 is 5.0V ±0.5V.

Capacitance

Symbol	Parameter	Typ	Units	Conditions
C_{IN}	Input Capacitance	4.5	pF	V_{CC} = OPEN
C_{PD}	Power Dissipation Capacitance	70	pF	V_{CC} = 5.0V

REVISION HISTORY

Changes from Revision A (April 2013) to Revision B	Page
• Changed layout of National Data Sheet to TI format	6

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