54F14

54F14 Hex Inverter Schmitt Trigger



Literature Number: SNOS153A



54F/74F14 Hex Inverter Schmitt Trigger

General Description

The 'F14 contains six logic inverters which accept standard TTL input signals and provide standard TTL output levels. They are capable of transforming slowly changing input signals into sharply defined, jitter-free output signals. In addition, they have a greater noise margin than conventional inverters.

Each circuit contains a Schmitt trigger followed by a Darlington level shifter and a phase splitter driving a TTL totempole output. The Schmitt trigger uses positive feed back to

effectively speed-up slow input transition, and provide different input threshold voltages for positive and negative-going transitions. This hysteresis between the positive-going and negative-going input thresholds (typically 800 mV) is determined internally by resistor ratios and is essentially insensitive to temperature and supply voltage variations.

Features

- Guaranteed 4000V minimum ESD protection
- Standard Military Drawing
 - 5962-88752

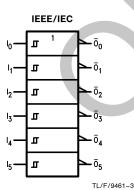
Commercial	Military	Package Number	Package Description
74F14PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line
	54F14DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line
74F14SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F14SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ
	54F14FM (Note 2)	W14B	14-Lead Cerpack
	54F14LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

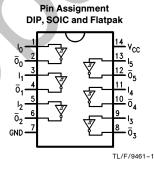
Note 1: Devices also available in 13" reel. Use Suffix = SCX and SJX.

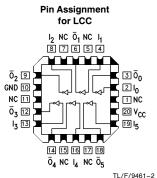
Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

Logic Symbol

Connection Diagrams







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Unit Loading/Fan Out

		54F/74F			
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}		
$\frac{I_n}{\overline{O}_n}$	Input Output	1.0/1.0 50/33.3	20 μA/ – 0.6 mA –1 mA/20 mA		

Function Table

Input	Output			
Α	ō			
L	Н			
Н	L			

H = HIGH Voltage Level
L = LOW Voltage Level



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 $\begin{array}{ll} \mbox{Storage Temperature} & -65\mbox{°C to} + 150\mbox{°C} \\ \mbox{Ambient Temperature under Bias} & -55\mbox{°C to} + 125\mbox{°C} \\ \end{array}$

Junction Temperature under Bias −55°C to +175°C

V_{CC} Pin Potential to

Ground Pin -0.5V to +7.0V

Input Voltage (Note 2) -0.5V to +7.0V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output

in HIGH State (with $V_{CC} = 0V$)

 $\begin{array}{lll} \text{Standard Output} & -0.5 \text{V to V}_{CC} \\ \text{TRI-STATE} \tiny{\circledR} \text{Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$

Current Applied to Output

in LOW State (Max) twice the rated I_{OL} (mA)

ESD Last Passing Voltage (Min) 4000V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature

Supply Voltage

Military + 4.5V to + 5.5V Commercial + 4.5V to + 5.5V

DC Electrical Characteristics

Symbol Parameter			54F/74F			Units	,	Conditions	
Syllibol	Parameter		Min	Тур	Max	Units	V _{CC}	Conditions	
V_{T+}	Positive-Going Threshold		1.5	1.7	2.0	V	5.0V		
V_{T-}	Negative-Going Threshold		0.7	0.9	1.1	V	5.0V		
ΔV_{T}	Hysteresis ($V_{T+} - V_{T-}$)		0.4	0.8		V	5.0V		
V _{CD}	Input Clamp Diode Voltage				-1.2	٧	Min	$I_{\text{IN}} = -18 \text{mA}$	
V _{OH}	Voltage 74F	10% V _{CC} 10% V _{CC} 5% V _{CC}	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}		10% V _{CC} 10% V _{CC}			0.5 0.5	٧	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
lін	Input HIGH 54F Current 74F				20.0 5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current 54F Breakdown Test 74F				100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH 54F Leakage Current 74F				250 50	μΑ	Max	V _{OUT} = V _{CC}	
V _{ID}	Input Leakage 74F		4.75			٧	Max	$I_{\text{ID}} = 1.9 \mu\text{A}$ All Other Pins Grounded	
l _{OD}	Output Leakage Circuit Current				3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
I _{IL}	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$	
los	Output Short-Circuit Current		-60		-150	mA	Max	$V_{OUT} = 0V$	
<u> Іссн</u>	Power Supply Current				25	mA	Max	V _O = HIGH	
_I _{CCL}	Power Supply Current				25	mA	Max	V _O = LOW	

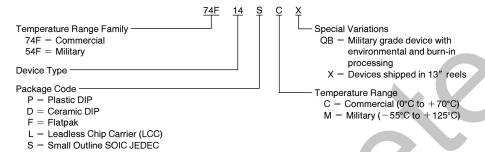
AC Electrical Characteristics

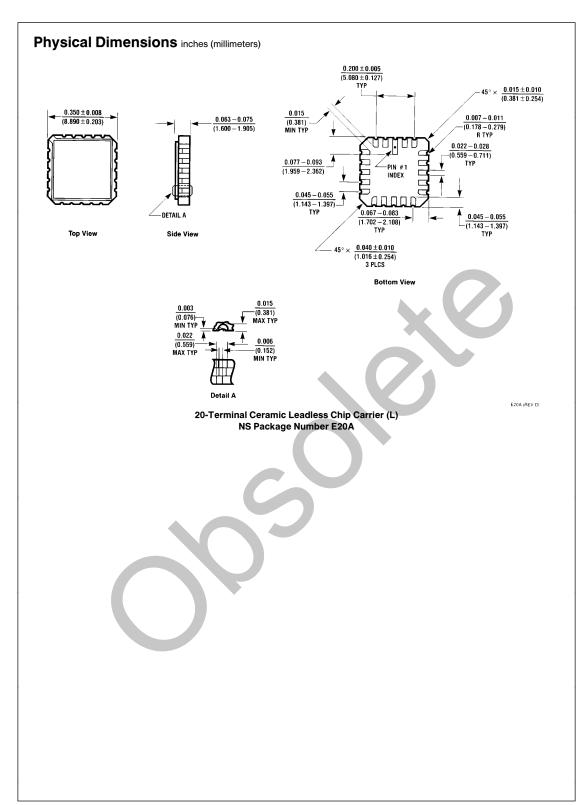
	Parameter	$74F \\ T_A = +25^{\circ}C \\ V_{CC} = +5.0V \\ C_L = 50 pF$		5	4F	74F		
Symbol				$ extsf{T}_{ extsf{A}}, extsf{V}_{ extsf{CC}} = extsf{Mil} \ extsf{C}_{ extsf{L}} = extsf{50 pF}$		T _A , V _{CC} = Com C _L = 50 pF		Units
		Min	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay	4.0	10.5	4.0	13.0	4.0	11.5	
t _{PHL}	$I_n \rightarrow \overline{O}_n$	3.5	8.5	3.5	10.0	3.5	9.0	ns

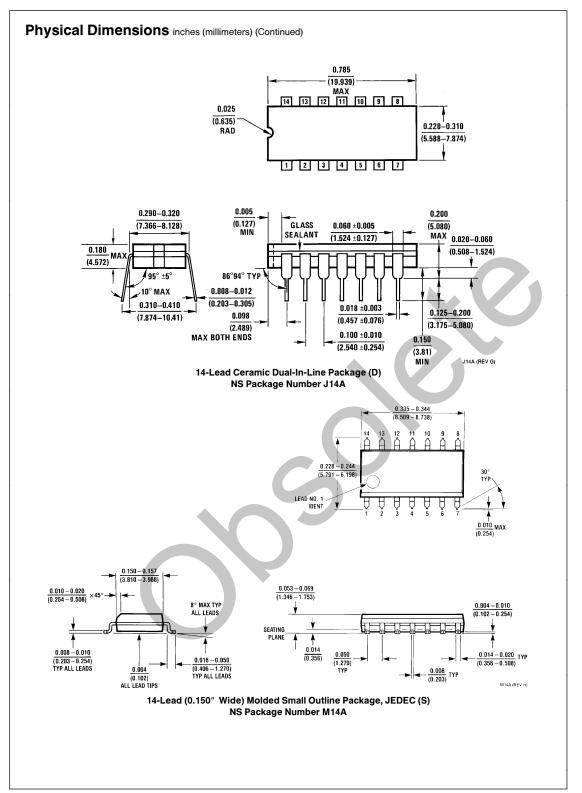
Ordering Information

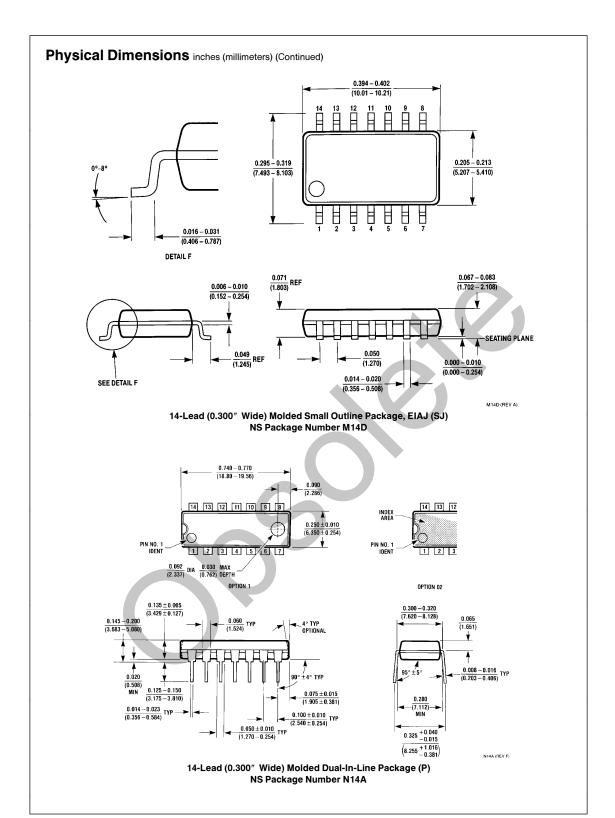
SJ = Small Outline SOIC EIAJ

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

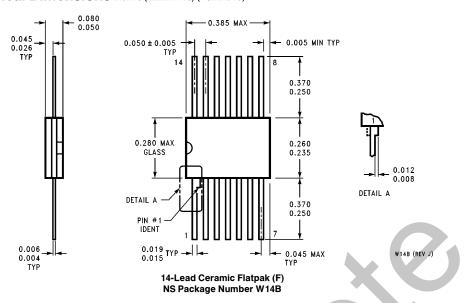








Physical Dimensions inches (millimeters) (Continued)



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