

UA592

MuA592 Differential Video Amplifier



Literature Number: SNOS252A

μ A592 Differential Video Amplifier

General Description

The μ A592 is a monolithic two-stage differential input, differential output video amplifier constructed using the Planar Epitaxial process. Internal series shunt feedback is used to obtain wide bandwidth, low phase distortion, and excellent gain stability. Emitter follower outputs enable the device to drive capacitive loads and all stages are current source biased to obtain high power supply and common mode rejection ratios.

The μ A592, in the 14-lead version, offers fixed gains of 100 and 400 without external components. A fixed gain of 400 is available in the 8-lead part. Adjustable gains from 0 to 400 are obtained with one external resistor.

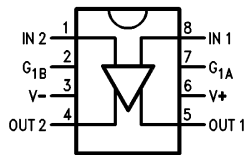
No external frequency compensation components are required for any gain option. The device is particularly useful in magnetic tape or disc file systems using phase or NRZ encoding. Other applications include general purpose video and pulse amplifiers.

Features

- 90 MHz bandwidth typ
- Selectable gains from 0 to 400 typ
- No frequency compensation required
- Adjustable pass band

Connection Diagrams

8-Lead DIP and SO-8 Package

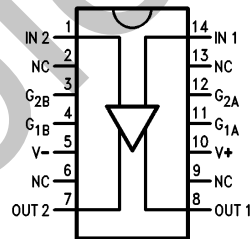


Top View

Order Number μ A592SC or μ A592TC
See NS Package Number M08E or N08E

TL/H/10047-1

14-Lead DIP



Top View

Order Number μ A592DC or μ A592DM
See NS Package Number J14A

TL/H/10047-2

Order Information

Device Code	Package Code	Package Description
μ A592TC	N08E	Molded DIP
μ A592SC	M08A	Molded Surface Mount
μ A592DM	J14A	Ceramic DIP
μ A592DC	J14A	Ceramic DIP

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature Range		
Ceramic DIP	–65°C to +175°C	
Molded DIP, SO-8	–65°C to +150°C	
Operating Temperature Range		
Extended (μ A592M)	–55°C to +125°C	
Commercial (μ A592C)	0°C to +70°C	
Lead Temperature		
Ceramic DIP (Soldering, 60 sec.)	300°C	
Molded DIP and SO Package (Soldering, 10 sec.)	265°C	

Internal Power Dissipation (Notes 1, 2)

8L-Molded DIP	0.93W
SO-8	0.81W
14L-Molded DIP	1.04W
14L-Ceramic DIP	1.36W
Supply Voltage	$\pm 8.0V$
Differential Input Voltage	$\pm 5.0V$
Common Mode Input Voltage	$\pm 6.0V$
Output Current	10 mA

μ A592 and μ A592C

Electrical Characteristics $T_A = 25^\circ C$, $V_{CC} = \pm 6.0V$ unless otherwise specified.

Symbol	Parameter	Conditions (Notes 3, 4)	μ A592			μ A592C			Units	
			Min	Typ	Max	Min	Typ	Max		
A _{VD}	Differential Voltage Gain	R _L = 2.0 k Ω , V _O = 3.0 V _{P-P}	Gain 1	300	400	500	250	400	600	V/V
			Gain 2	90	100	110	80	100	120	
B _W	Bandwidth	R _S = 50 Ω	Gain 1		40			40		MHz
			Gain 2		90			90		
t _r	Risetime	R _S = 50 Ω , V _O = 1.0 V _{P-P}	Gain 1		10.5			10.5		ns
			Gain 2		4.5	10		4.5	12	
t _{pD}	Propagation Delay	R _S = 50 Ω , V _O = 1.0 V _{P-P}	Gain 1		7.5			7.5		ns
			Gain 2		6.0	10		6.0	10	
Z _I	Input Impedance		Gain 1		4.0			4.0		k Ω
			Gain 2	20	30		10	30		
C _I	Input Capacitance			2.0			2.0		pF	
I _{IO}	Input Offset Current			0.4	3.0		0.4	5.0	μ A	
I _{IB}	Input Bias Current			9.0	20		9.0	30	μ A	
e _n	Input Noise Voltage	R _S = 50 Ω , BW = 1.0 kHz to 10 MHz		12			12		μ V _{rms}	
V _{IR}	Input Voltage Range		± 1.0			± 1.0			V	
CMR	Common Mode Rejection	V _{CM} = 1.0V, Gain 2	60	86		60	86		dB	
PSRR	Power Supply Rejection Ratio	$\Delta V_{CC} = \pm 0.5V$, Gain 2	50	70		50	70		dB	
V _{OO}	Output Offset Voltage		Gain 1		0.6	1.5		0.6	1.5	V
			Gain 2		0.35	0.75		0.35	0.75	
V _{OCM}	Output Common Mode Voltage		2.4	2.9	3.4	2.4	2.9	3.4	V	
V _{OP}	Output Voltage Swing		3.0	4.0		3.0	4.0		V _{P-P}	
I _{O-}	Output Sink Current		2.5	3.6		2.5	3.6		mA	
R _O	Output Resistance			20			20		Ω	
I _{CC}	Supply Current			18	24		18	24	mA	

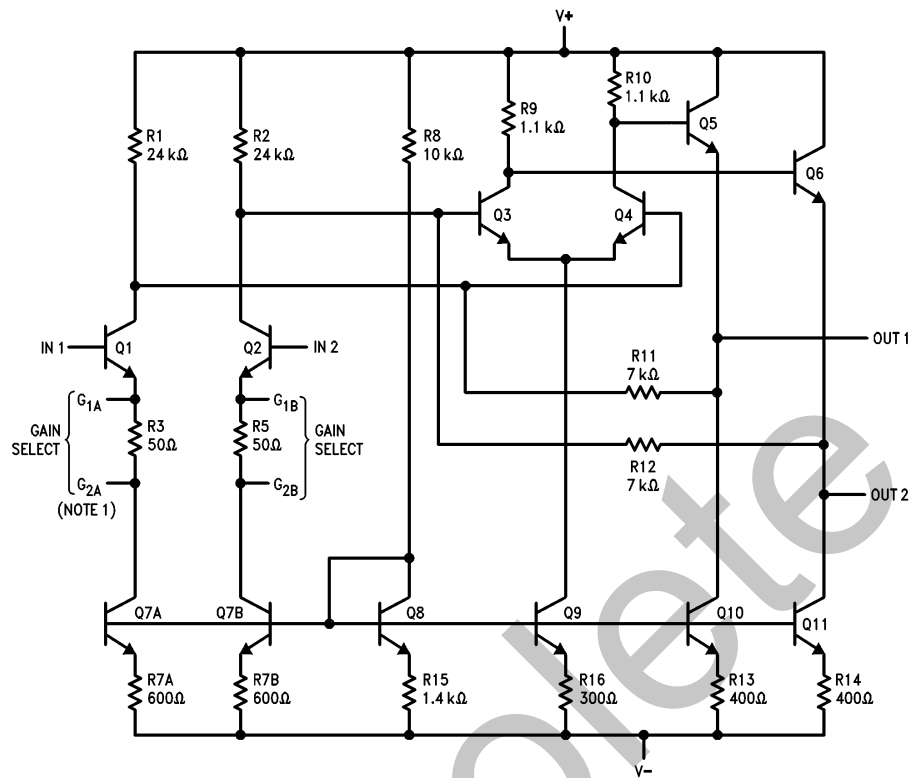
Note 1: T_{J Max} = 150°C for the Molded DIP and SOIC, and 175°C for the Ceramic DIP.

Note 2: Ratings apply to ambient temperature at 25°C. Above this temperature, derate the 8L-Molded DIP at 7.5 mW/°C, the SO-8 at 6.5 mW/°C, the 14L-Molded DIP at 8.3 mW/°C, and the 14L-Ceramic DIP at 9.1 mW/°C.

Note 3: Gain Select leads G_{1A} and G_{1B} connected together for Gain 1 and Gain Select leads G_{2A} and G_{2B} connected together for Gain 2.

Note 4: Gain 2 not applicable to 8 lead device.

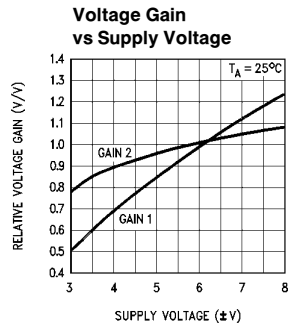
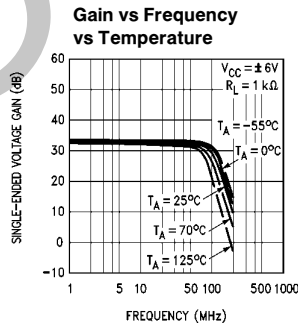
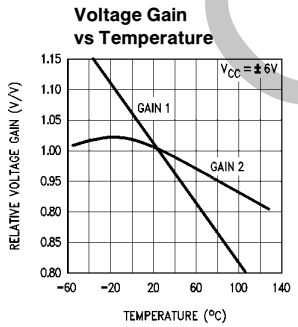
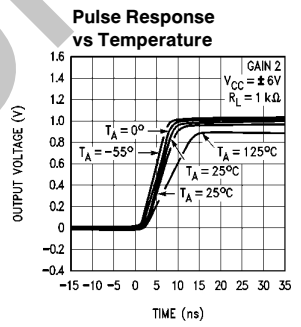
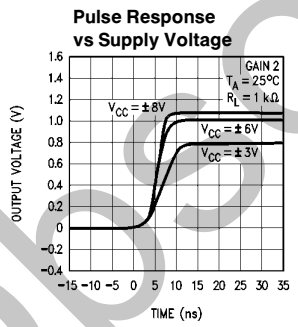
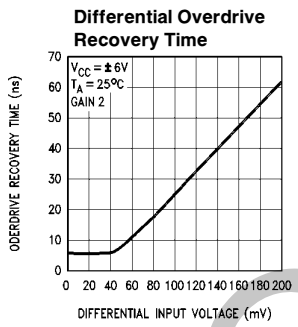
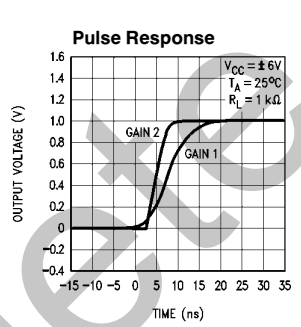
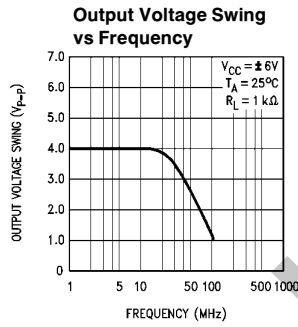
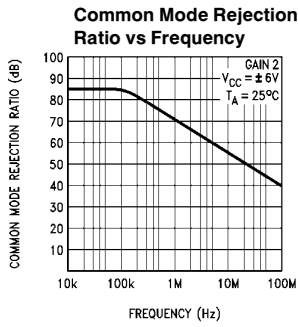
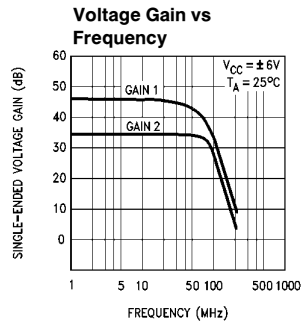
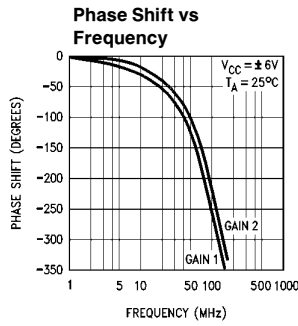
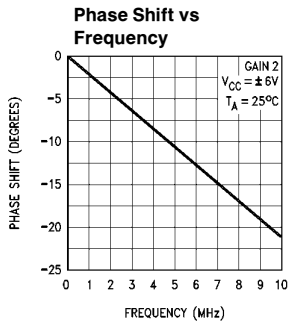
Equivalent Circuit



Note 1: G_{2A} and G_{2B} applies to 14 lead device only.

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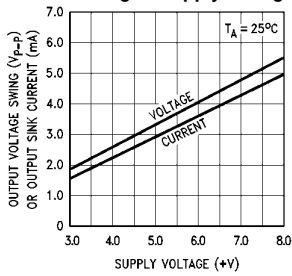
Typical Performance Curves



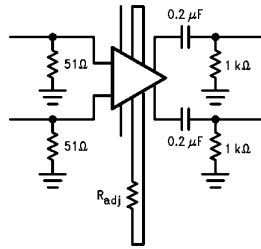
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Typical Performance Curves (Continued)

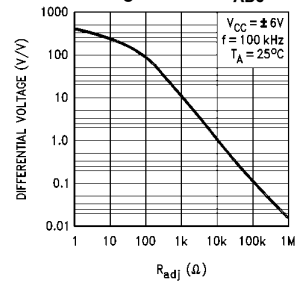
Output Voltage and Current Swing vs Supply Voltage



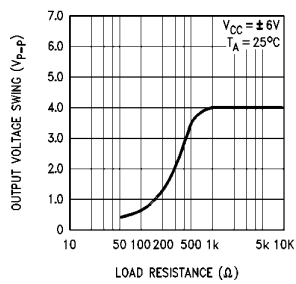
Voltage Gain Adjust Circuit



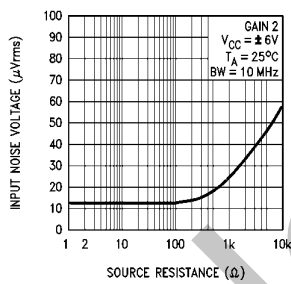
Voltage Gain vs R_{ADJ}



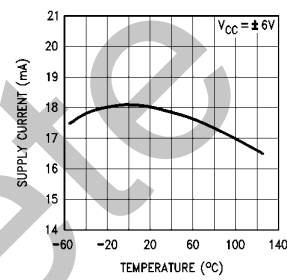
Output Voltage Swing vs Load Resistance



Input Noise Voltage vs Source Resistance

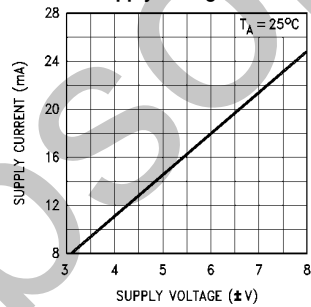


Supply Current vs Temperature



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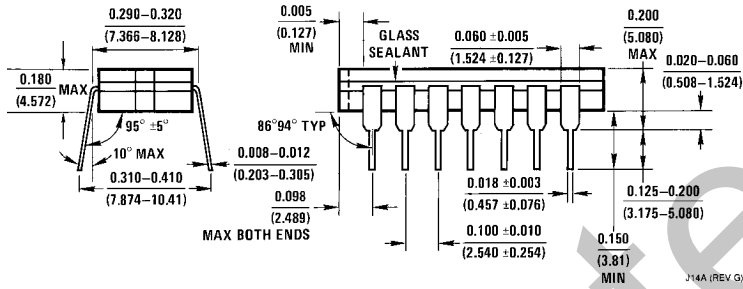
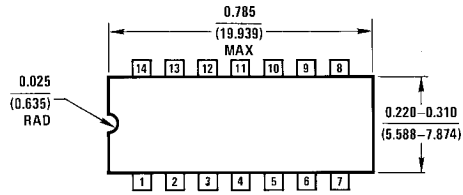
Supply Current vs Supply Voltage



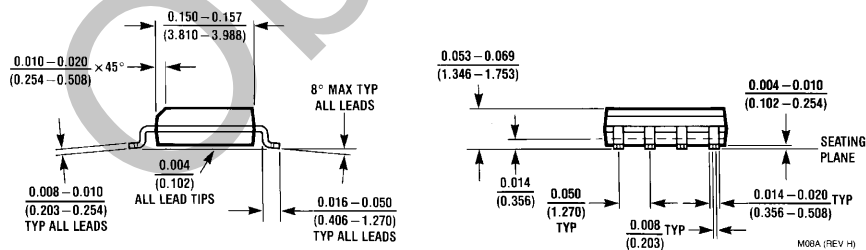
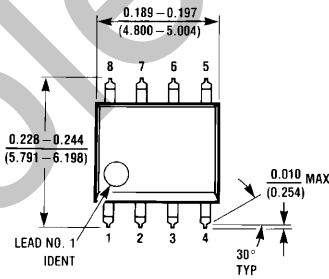
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Obsolete

Physical Dimensions inches (millimeters)

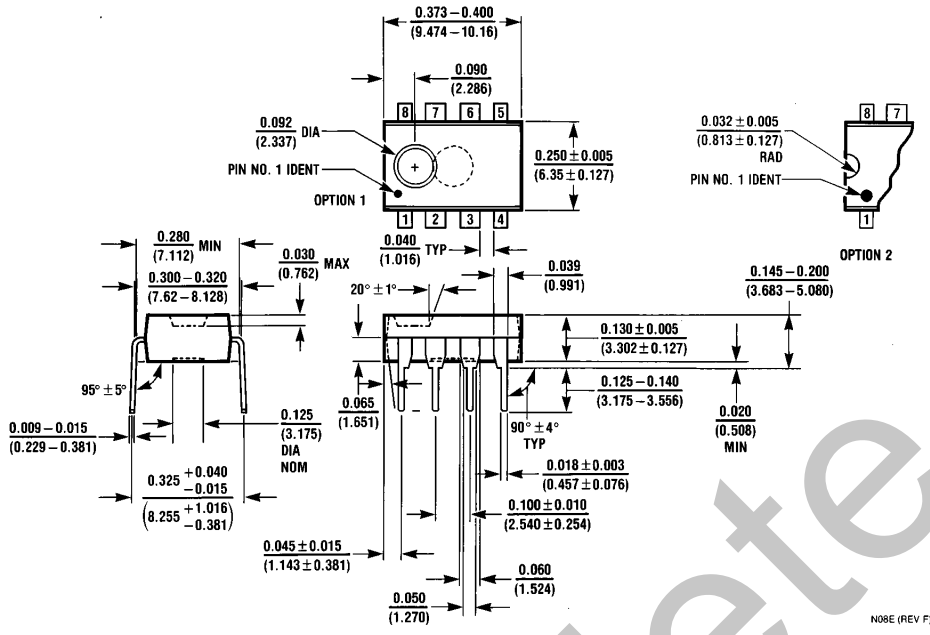


14-Lead Ceramic Dual-In-Line Package (J)
 Order Number μ A592DC or μ A592DM
 NS Package Number J14A



8-Lead Molded Surface Mount (M)
 Order Number μ A592SC
 NS Package Number M08A

Physical Dimensions inches (millimeters) (Continued)



8-Lead Molded Dual-In-Line Package (N)
Order Number μA592TC
NS Package Number N08E

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National Semiconductor Corporation
 1111 West Bardin Road
 Arlington, TX 76017
 Tel: (800) 272-9959
 Fax: (1-800) 737-7018

National Semiconductor Europe
 Fax: (+49) 0-180-530 85 86
 Email: cnjwge@tevm2.nsc.com
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National Semiconductor Hong Kong Ltd.
 19th Floor, Straight Block,
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