

Power Management Unit with DCDC Controller

Check for Samples: [TPS659110](#), [TPS659112](#), [TPS659113](#)

FEATURES

The purpose of the TPS659110 device is to provide the following resources:

- **Embedded power controller (EPC) with EEPROM programmability**
- **Two efficient step-down DCDC converters for processor cores (VDD1, VDD2)**
- **One efficient step-down DCDC converter for I/O power (VIO)**
- **One controller for external FETs (VDDCtrl)**
- **Dynamic voltage scaling for processor cores**
- **Eight LDO voltage regulators and one RTC LDO (supply for internal RTC)**
- **One high-speed I²C interface for general-purpose control commands (CTL-I²C)**
- **Two independent enable signals for controlling power resources (EN1, EN2). Alternatively, these pins can be used as a high-speed I²C interface dedicated for voltage scaling for VDD1 and VDD2.**
- **Thermal shutdown protection and hot-die detection**
- **A real-time clock (RTC) resource with:**
 - **Oscillator for 32.768-kHz crystal or 32-kHz built-in RC oscillator**
 - **Date, time and calendar**
 - **Alarm capability**
- **Nine configurable GPIOs with multiplexed feature support:**
 - **Four can be used as enable for external resources, included into power up sequence and controlled by state-machine.**
 - **As GPI, GPIOs support logic-level detection and can generate maskable interrupt for wake-up.**
 - **Two of the GPIOs have 10 mA current sink capability for driving LEDs.**
 - **DCDCs switching synchronization through an external 3-MHz clock.**
- **Two reset inputs, for cold reset (HDRST) and a**

power initialization reset (PWRDN) for thermal reset input

- **32-kHz clock and reset (NRESPWRON) for system and an additional output for reset signal**
- **Watchdog**
- **Two ON/OFF LED pulse generators and one PWM generator**
- **Two comparators for system control, connected to VCCS pin**
- **A JTAG[®] and boundary scan, but not accessible in functional mode (test purpose)**

APPLICATIONS

- **Portable and handheld systems**

DESCRIPTION

The TPS659110 is an integrated Power Management IC available in 98-pin 0.65-mm pitch BGA package and dedicated to applications powered by one Li-Ion or Li-Ion polymer battery cell or 3-series Ni-MH cells or a 5 V input, and which require multiple power rails. The device provides three step-down converters, one controller for external FETs to support high current rail, eight LDOs, and is designed to be flexible PMIC for supporting different processors and applications.

Two of the step-down converters provide power for dual processor cores and support dynamic voltage scaling by a dedicated I²C interface for optimum power savings. The third converter provides power for the I/Os and memory in the system.

The device includes eight general-purpose LDOs providing a wide range of voltage and current capabilities. Five of the LDOs support 1.0 to 3.3 V with 100-mV step and three (LDO1, LDO2, LDO4) support 1.0 to 3.3 V with 50-mV step. All LDOs are fully controllable by the I²C interface.

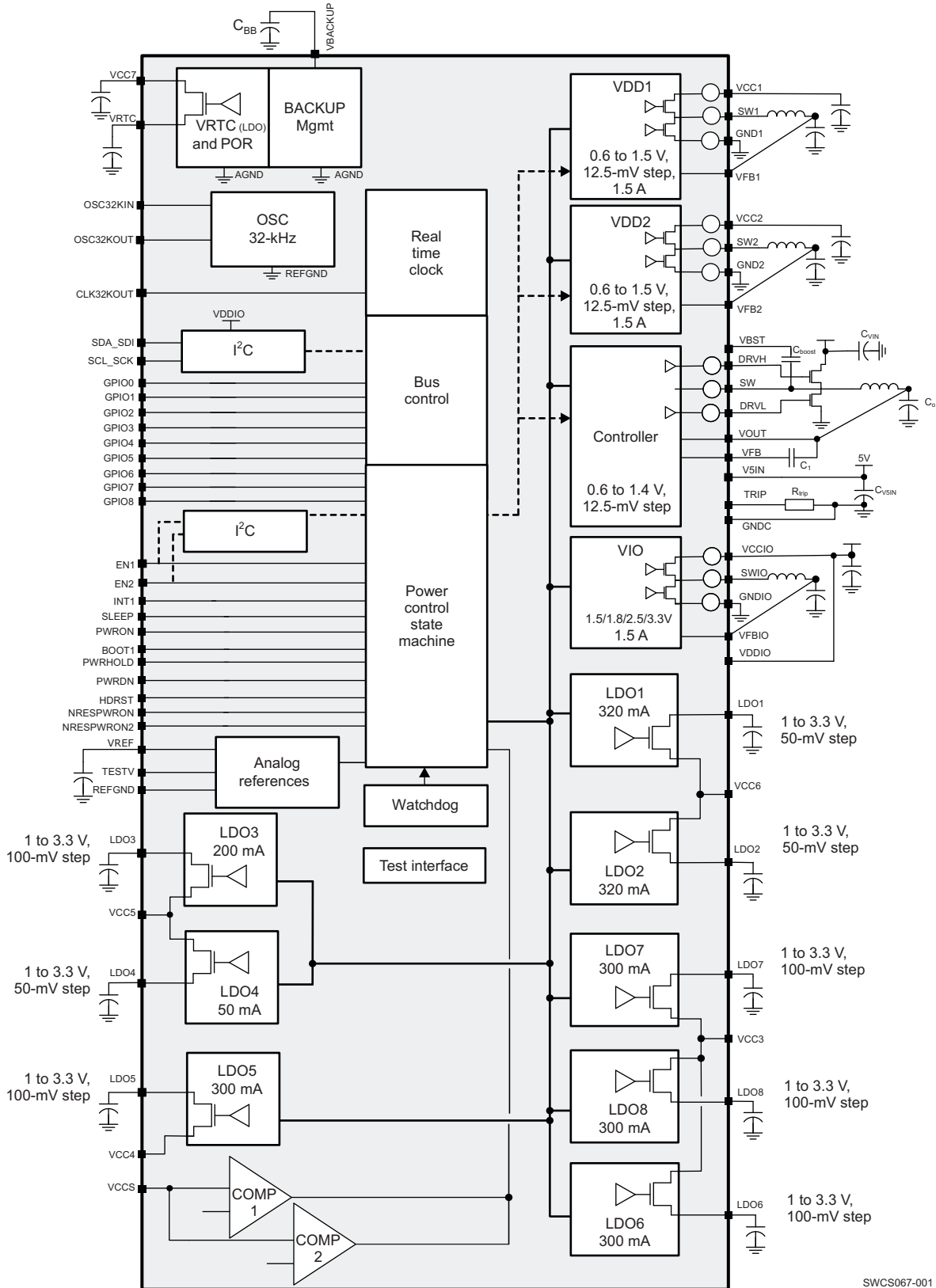
In addition to the power resources, the device contains an EPC to manage the power sequencing requirements of systems and an RTC. Power sequencing is programmable by EEPROM.

Figure 1 shows the top-level diagram of the device.



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SWCS067-001

Figure 1. Top-Level Diagram

For the complete TPS65911 data sheet, contact your TI sales representative. The document is internally available for download on ESP under the corresponding TPS659110/2/3 product folders and can be shared with customers.

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US ECCN: EAR99

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PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
TPS6591102A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591102A2	Samples
TPS6591102A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591102A2	Samples
TPS6591102AA2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591102AA2	Samples
TPS6591102AA2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591102AA2	Samples
TPS6591103A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591103A2	Samples
TPS6591103A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591103A2	Samples
TPS6591104A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591104A2	Samples
TPS6591104A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591104A2	Samples
TPS6591104DA2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591104DA2	Samples
TPS6591104DA2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591104DA2	Samples
TPS6591104EA2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	T6591104EA2	Samples
TPS6591104EA2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	T6591104EA2	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
TPS6591106A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	T6591106A2	Samples
TPS6591106A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	T6591106A2	Samples
TPS6591109A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591109A2	Samples
TPS6591109A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591109A2	Samples
TPS659110A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659110A2	Samples
TPS659110A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659110A2	Samples
TPS659112A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659112A2	Samples
TPS659112A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659112A2	Samples
TPS6591133A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591133A2	Samples
TPS6591133A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS6591133A2	Samples
TPS659113A2ZRC	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	240	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659113A2	Samples
TPS659113A2ZRCR	ACTIVE	BGA MICROSTAR JUNIOR	ZRC	98	2500	Green (RoHS & no Sb/Br)	CUNIAU	Level-3-260C-168 HR	-40 to 85	TPS659113A2	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

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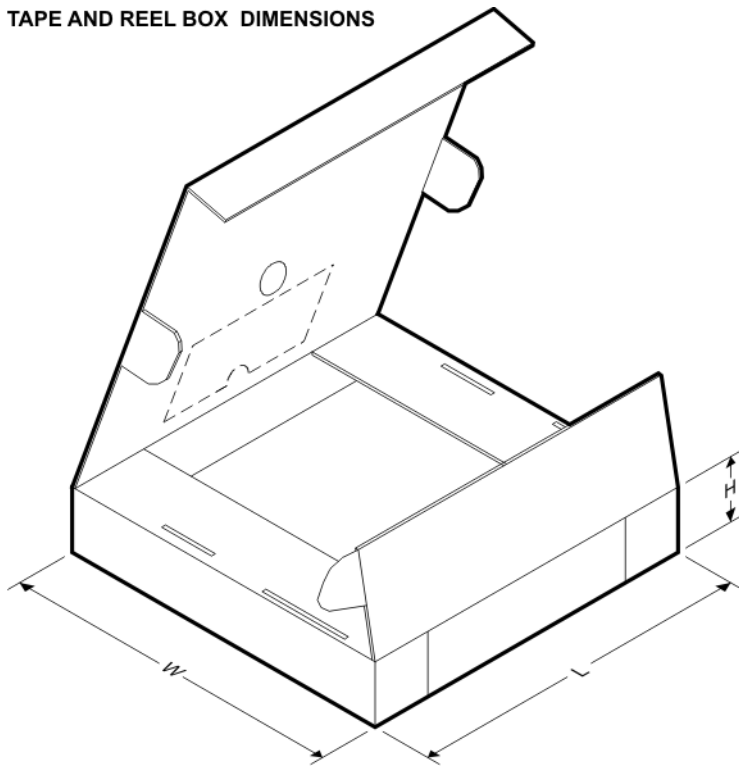
TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS6591102A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS6591104A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS6591104EA2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS6591106A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS6591109A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS659110A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	OR											
TPS659112A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS6591133A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1
TPS659113A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	330.0	16.4	6.3	9.3	1.5	12.0	16.0	Q1

TAPE AND REEL BOX DIMENSIONS


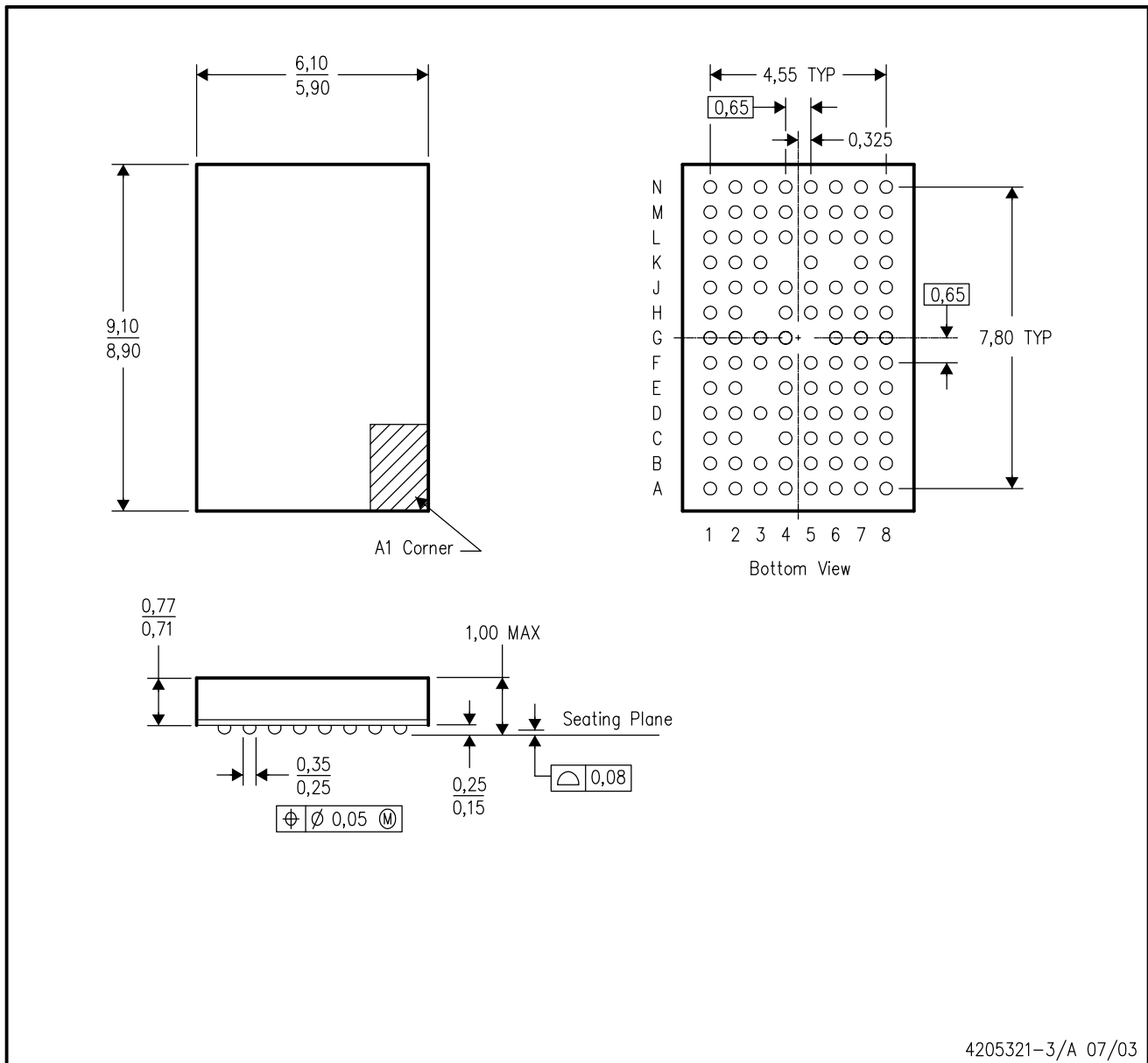
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS6591102A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS6591104A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS6591104EA2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS6591106A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS6591109A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS659110A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS659112A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS6591133A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8
TPS659113A2ZRCR	BGA MICROSTAR JUNIOR	ZRC	98	2500	336.6	336.6	31.8

ZRC (S-PBGA-N98)

PLASTIC BALL GRID ARRAY



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. MicroStar Junior™ BGA configuration
 - D. Falls within JEDEC MO-225
 - E. This package is lead-free.

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